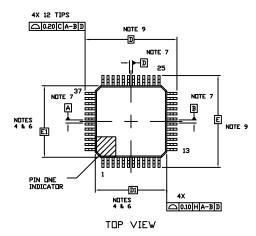
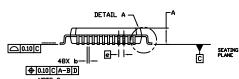


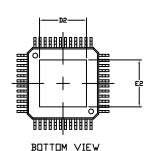
SPQFP48 7x7 / SQFP48K CASE 131AN ISSUE A

DATE 08 NOV 2013



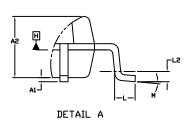




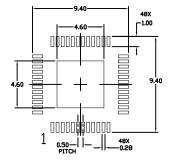


NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION MILLIMETERS
- DIMENSION 6 DOES NOT INCLUDE DAMBAR PROTRUSION. DAMBAR PROTRUSION SHALL BE 0.08 MAX. AT MMC. DAMBAR CANNOT BE LOCATED ION THE LOVER RADIUS OF THE FOOT. MINIMUM SPACE BETVEEN PROTRUSION AND ADJACENT LEAD IS 0.07.
- DIMENSIONS DI AND EL DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.25 PER SIDE. DIMENSIONS DI AND EL ARE MAXIMUM PLASTIC BODY SIZE INCLUDING MOLD MISMATCH.
- THE TOP PACKAGE BODY SIZE MAY BE SMALLER THAN THE BUTTOM PACKAGE SIZE BY AS MUCH AS 0.15.
- DATUMS A-B AND D ARE DETERMINED AT DATUM PLANE H.
- A1 IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.
- DIMENSIONS D AND E TO BE DETERMINED AT DATUM PLANE C.



	MILLIMETERS		
DIM	MIN.	MAX.	
Α		1.70	
A1	0.00	0.15	
A2	1.50 REF		
b	0.15	0.26	
D	9.00 BSC		
D1	7.00 BSC		
D2	4.60 REF		
Ε	9.00 BSC		
E1	7.00 BSC		
E2	4.60 REF		
e	0.50 BSC		
L	0.30	0.70	
L2	0.25 BSC		
М	ô	10*	



RECOMMENDED MOUNTING FOOTPRINT

DOCUMENT NUMBER:	98AON78439F	Electronic versions are uncontrolled except when accessed directly from Printed versions are uncontrolled except when stamped "CONTROLLED"	
DESCRIPTION:	SPQFP48 7X7 / SQFP48K		PAGE 1 OF 1

ON Semiconductor and unare trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.