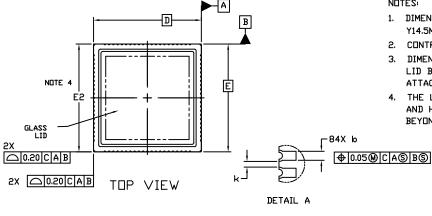
## LCC84, 19x19 CASE 115BC **ISSUE A**

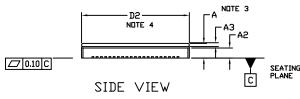
**DATE 26 JUN 2015** 

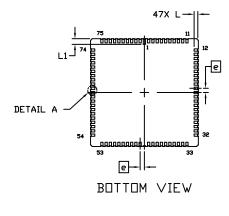


## NOTES:

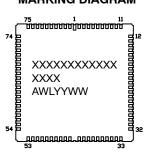
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSION A INCLUDES THE PACKAGE BODY AND LID BUT DOES NOT INCLUDE HEATSINKS OR OTHER ATTACHED FEATURES.
- THE LID IS DEFINED BY DIMENSIONS D2 AND E2 AND HAS A MAXIMUM ALLOWABLE SHIFT OF 0.60 BEYOND DIMENSIONS D AND E.

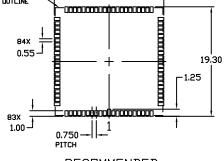
MILLIMETERS		
MIN.	MAX.	
2.08	2.74	
1.78	REF	
0.55	REF	
0.32	0.48	
19.00	BSC	
19.00	REF	
19.00	BSC	
19.00	REF	
0.75	BSC	
0.12		
0.62	0.88	
1.00	REF	
	MIN. 2.08 1.78 0.55 0.32 19.00 19.00 19.00 0.75 0.12 0.62	











RECOMMENDED MOUNTING FOOTPRINT

XXXXX = Specific Device Code = Assembly Location

= Wafer Lot WL YY = Year WW = Work Week

	I CC84 19 00 X 19 00	Printed versions are uncontrolled except when stamped "CONTROLLED	DAGE 1 OF 1
DOCUMENT NUMBER: 98AON82963F		Electronic versions are uncontrolled except when accessed directly from the Document Repository.  Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	

ON Semiconductor and un are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.