

# **Ultrafast Dual Diode**

12 A, 200 V

# **RURD620CCS9A**

The RURD620CCS9A is an ultrafast dual diode with low forward voltage drop. This device is intended for use as freewheeling and clamping diodes in a variety of switching power supplies and other power switching applications. It is specially suited for use in switching power supplies and industrial application.

#### **Features**

- Ultrafast Recovery  $t_{rr} = 30 \text{ ns}$  (@  $I_F = 6 \text{ A}$ )
- Max Forward Voltage,  $V_F = 1.0 \text{ V}$  (@  $T_C = 25^{\circ}\text{C}$ )
- Reverse Voltage, V<sub>RRM</sub> = 200 V
- Avalanche Energy Rated
- RoHS Compliant

## **Applications**

- Switching Power Supplies
- Power Switching Circuits
- General Purpose

## ABSOLUTE MAXIMUM RATINGS (Per Leg)

 $(T_C = 25^{\circ}C \text{ unless otherwise specified})$ 

Symbol	Rating	Value	Unit
$V_{RRM}$	Peak Repetitive Reverse Voltage	200	V
V <sub>RWM</sub>	Working Peak Reverse Voltage	200	V
$V_{R}$	DC Blocking Voltage	200	V
I <sub>F(AV)</sub>	Average Rectified Forward Current T <sub>C</sub> = 160°C	6	Α
I <sub>FRM</sub>	Repetitive Peak Surge Current Square Wave, 20 kHz	12	Α
I <sub>FSM</sub>	Nonrepetitive Peak Surge Current Halfwave, 1 Phase, 60 Hz	60	Α
$P_{D}$	Maximum Power Dissipation	45 W	
E <sub>AVL</sub>	Avalanche Energy (See Figures 10 and 11)	10 mJ	
$T_{STG}$ , $T_{J}$	Operating and Storage Temperature	–65 to 175 °C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## SYMBOL





DPAK3 (TO-252 3 LD) JEDEC CASE 369AS

#### **MARKING DIAGRAM**



\$Y = onsemi Logo

&Z = Assembly Plant Code &3 = 3-Digit Date Code

&K = 2-Digits Lot Run Traceability Code

XXXXX = Device Code (UR620C, RURD620)

## ORDERING INFORMATION

Part Number	Package	Brand
RURD620CCS9A	TO-252-3L	UR620C
RURD620CCS9A-F085	TO-252-3L	RURD620

NOTE: When ordering, use the entire part number.
Add the suffix, 9 A, to obtain the TO-252
variant in tape and reel, i.e.,
RURD620CCS9A.

## RURD620CCS9A

## **ELECTRICAL CHARACTERISTICS** (Per Leg) (T<sub>C</sub> = 25°C unless otherwise specified)

Symbol	Test Condition	Min	Тур	Max	Unit
V <sub>F</sub>	I <sub>F</sub> = 6 A	-	_	1.0	V
	I <sub>F</sub> = 6 A, T <sub>C</sub> = 150°C	-	-	0.83	V
I <sub>R</sub>	V <sub>R</sub> = 200 V	-	-	100	μΑ
	V <sub>R</sub> = 200 V, T <sub>C</sub> = 150°C	-	=	500	μΑ
t <sub>rr</sub>	$I_F = 1 \text{ A, } dI_F/dt = 200 \text{ A/}\mu\text{s}$	-	-	25	ns
	$I_F = 6 \text{ A}, dI_F/dt = 200 \text{ A}/\mu\text{s}$	-	=	30	ns
ta	I <sub>F</sub> = 6 A, dI <sub>F</sub> /dt = 200 A/μs	-	13	-	ns
t <sub>b</sub>	$I_F = 6 \text{ A}, dI_F/dt = 200 \text{ A}/\mu\text{s}$	-	6.5	-	ns
Q <sub>rr</sub>	$I_F = 6 \text{ A}, dI_F/dt = 200 \text{ A}/\mu\text{s}$	-	20	-	nC
CJ	V <sub>R</sub> = 10 V, I <sub>F</sub> = 0 A	-	30	-	pF
$R_{ heta JC}$		-	-	3.5	°C/W

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **DEFINITIONS**

 $V_F$  = Instantaneous forward voltage (pw = 300  $\mu$ s, D = 2%).

I<sub>R</sub> = Instantaneous reverse current.

 $T_{rr}$  = Reverse recovery time (See Figure 9), summation of  $t_a + t_b$ .

t<sub>a</sub> = Time to reach peak reverse current (See Figure 9).

 $t_b$  = Time from peak  $I_{RM}$  to projected zero crossing of  $I_{RM}$  based on a straight line from peak  $I_{RM}$  through 25% of  $I_{RM}$  (See Figure 9).

 $Q_{rr}$  = Reverse recovery charge.

C<sub>J</sub> = Junction Capacitance.

 $R_{\theta JC}$  = Thermal resistance junction to case. pw = Pulse width.

D = Duty cycle.

## TYPICAL PERFORMANCE CURVES

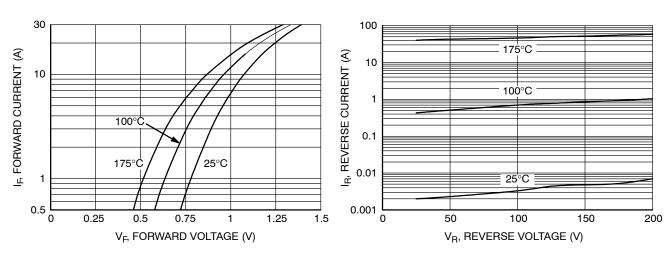


Figure 1. Forward Current vs. Forward Voltage

Figure 2. Reverse Current vs. Reverse Voltage

## RURD620CCS9A

## TYPICAL PERFORMANCE CURVES (Continued)

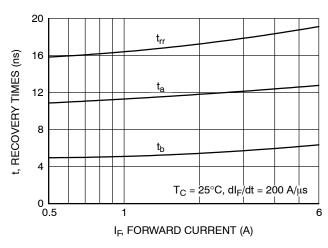


Figure 3. t<sub>rr</sub>, t<sub>a</sub> and t<sub>b</sub> Curves vs. Forward Current

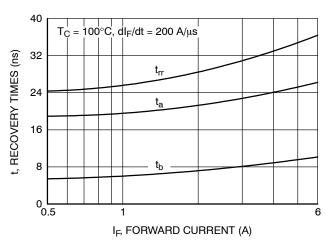


Figure 4.  $t_{rr}$ ,  $t_a$  and  $t_b$  Curves vs. Forward Current

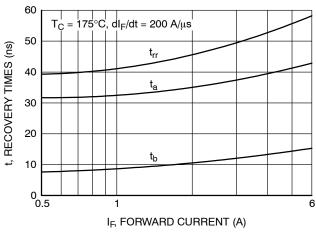


Figure 5. t<sub>rr</sub>, t<sub>a</sub> and t<sub>b</sub> Curves vs. Forward Current

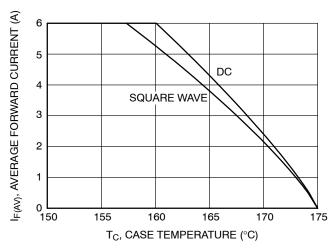


Figure 6. Current Derating Curve

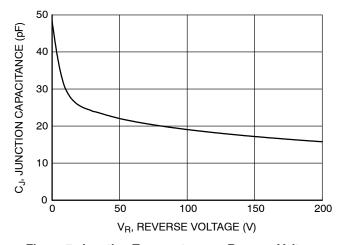


Figure 7. Junction Temperature vs. Reverse Voltage

## RURD620CCS9A

## **TEST CIRCUITS AND WAVEFORMS**

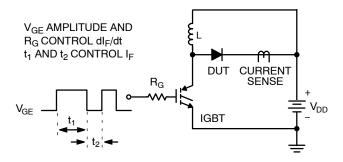


Figure 8. t<sub>rr</sub> Test Circuit

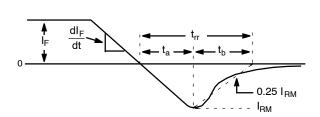


Figure 9. t<sub>rr</sub> Waveforms and Definitions

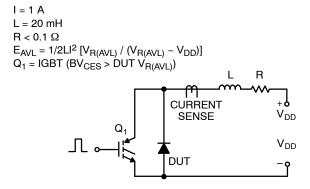


Figure 10. Avalanche Energy Test Circuit

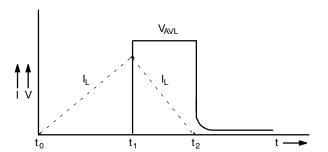
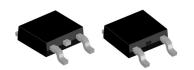


Figure 11. Avalanche Current and Voltage Waveforms





### DPAK3 6.10x6.54x2.29, 4.57P CASE 369AS **ISSUE B**

**DATE 20 DEC 2023** 



- NOTES: UNLESS OTHERWISE SPECIFIED

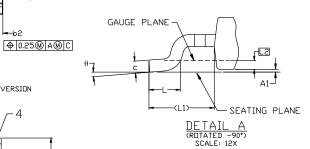
  A) THIS PACKAGE CONFORMS TO JEDEC, TO-252, ISSUE F, VARIATION AA.

  B) ALL DIMENSIONS ARE IN MILLIMETERS.

  C) DIMENSIONING AND TOLERANCING PER

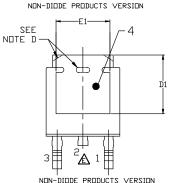
  - מו
  - A
  - F)

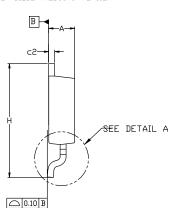
  - DIMENSIONING AND TOLERANCING PER
    ASME Y14.5M-2018.
    SUPPLIER DEPENDENT MOLD LOCKING HOLES OR CHAMFERED
    CORNERS OR EDGE PROTRUSION.
    FOR DIGDE PRODUCTS, L4 IS 0.25 MM MAX PLASTIC BODY
    STUB WITHOUT CENTER LEAD.
    DIMENSIONS ARE EXCLUSIVE OF BURRS,
    MOLD FLASH AND TIE BAR EXTRUSIONS.
    LAND PATTERN RECOMMENDATION IS BASED ON IPC7351A STD
    T0228P991X239-3N.



MIN. N□M. MAX A 2.18 2.29 2.3 A1 0.00 - 0.16 b 0.64 0.77 0.89	9			
A1 0.00 - 0.12	-			
	7			
la 064 077 009	- /			
N	9			
b2 0.76 0.95 1.14				
b3 5.21 5.34 5.4	6			
c 0.45 0.53 0.63	ı			
c2 0.45 0.52 0.5t	3			
D 5.97 6.10 6.2	2			
D1 5.21	-			
E 6.35 6.54 6.7	3			
E1 4.32	-			
e 2.286 BSC	2.286 BSC			
e1 4.572 BSC	4.572 BSC			
H 9.40 9.91 10.4	1			
L 1.40 1.59 1.78	3			
L1 2.90 REF	2.90 REF			
L2 0.51 BSC	0.51 BSC			
L3 0.89 1.08 1.27	7			
L4 1.0a	2 ]			
θ 0° 10°				

MILLIMETERS





<del>-</del> 5.55	MIN-
6.40	6.50 MIN
	2.85 MIN
4.5	1.25 MIN 2.286

#### LAND PATTERN RECOMMENDATION

\*FOR ADDITIONAL INFORMATION ON DUR
PB-FREE STRATEGY AND SOLDERING DETAILS,
PLEASE DOWNLOAD THE ON SEMICONDUCTOR
SOLDERING AND MOUNTING TECHNIQUES
REFERENCE MANUAL, SOLDERRM/D.

## **GENERIC MARKING DIAGRAM\***

XXXXXX XXXXXX **AYWWZZ** 

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

XXXX = Specific Device Code

= Assembly Location Α

Υ = Year

WW = Work Week

77 = Assembly Lot Code

Electronic versions are uncontrolled except when accessed directly from the Document Repository. **DOCUMENT NUMBER:** 98AON13810G Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. **DESCRIPTION:** DPAK3 6.10x6.54x2.29, 4.57P **PAGE 1 OF 1** 

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

#### ADDITIONAL INFORMATION

**TECHNICAL PUBLICATIONS:** 

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$ 

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales