## onsemi

## Dual General Purpose Transistor NST3904DXV6T1G, NSVT3904DXV6T1G, NST3904DXV6T5G

The NST/NSV3904DXV6 device is a spin-off of our popular SOT-23/SOT-323 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-563 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

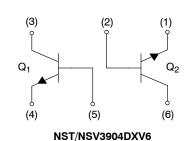
#### Features

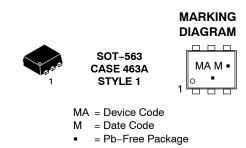
- h<sub>FE</sub>, 100–300
- Low  $V_{CE(sat)}$ ,  $\leq 0.4 \text{ V}$
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- AEC-Q101 Qualified and PPAP Capable NSVT3904DXV6T1G
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements

#### MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CEO</sub>	40	Vdc
Collector - Base Voltage		V <sub>CBO</sub>	60	Vdc
Emitter – Base Voltage		V <sub>EBO</sub>	6.0	Vdc
Collector Current – Continuous		Ι <sub>C</sub>	200	mAdc
Electrostatic Discharge	HBM MM	ESD	>16000 >2000	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.





(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NST3904DXV6T1G	SOT-563 (Pb-Free)	4000/Tape & Reel
NSVT3904DXV6T1G	SOT-563 (Pb-Free)	4000/Tape & Reel
NST3904DXV6T5G	SOT-563 (Pb-Free)	8000/Tape & Reel
SNST3904DXV6T1G	SOT-563 (Pb-Free)	4000/Tape & Reel
SNST3904DXV6T5G	SOT-563 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### THERMAL CHARACTERISTICS

Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C (Note 1)	PD	357 2.9	mW mW/°C
Thermal Resistance Junction-to-Ambient (Note 1)	R <sub>θJA</sub>	350	°C/W
Characteristic (Both Junctions Heated)	Symbol	Мах	Unit
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$ (Note 1)	PD	500 4.0	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{ hetaJA}$	250	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

1. FR-4 @ Minimum Pad

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (Note 2) ( $I_C = 1.0 \text{ mAdc}, I_B = 0$ )	V <sub>(BR)CEO</sub>	40	-	Vdc
Collector – Base Breakdown Voltage (I <sub>C</sub> = 10 $\mu$ Adc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	60	-	Vdc
Emitter – Base Breakdown Voltage (I <sub>E</sub> = 10 $\mu$ Adc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	6.0	-	Vdc
Base Cutoff Current (V <sub>CE</sub> = 30 Vdc, V <sub>EB</sub> = 3.0 Vdc)	I <sub>BL</sub>	-	50	nAdc
Collector Cutoff Current ( $V_{CE}$ = 30 Vdc, $V_{EB}$ = 3.0 Vdc)	I <sub>CEX</sub>	-	50	nAdc
ON CHARACTERISTICS (Note 2)				
DC Current Gain	h <sub>FE</sub>			_

		ΠE	40 70 100 60 30	- 300 - -		
C	Collector – Emitter Saturation Voltage ( $I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$ ) ( $I_C = 50 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$ )	V <sub>CE(sat)</sub>	-	0.2 0.3	Vdc	
E	base – Emitter Saturation Voltage ( $I_C = 10 \text{ mAdc}$ , $I_B = 1.0 \text{ mAdc}$ ) ( $I_C = 50 \text{ mAdc}$ , $I_B = 5.0 \text{ mAdc}$ )	V <sub>BE(sat)</sub>	0.65	0.85 0.95	Vdc	

#### SMALL-SIGNAL CHARACTERISTICS

Current-Gain – Bandwidth Product (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 20 Vdc, f = 100 MHz)	fт	300	_	MHz
Output Capacitance (V <sub>CB</sub> = 5.0 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	-	4.0	pF
Input Capacitance (V <sub>EB</sub> = 0.5 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ibo</sub>	-	8.0	pF
Input Impedance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 1.0 kHz)	h <sub>ie</sub>	1.0 2.0	10 12	kΩ
Voltage Feedback Ratio (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 1.0 kHz)	h <sub>re</sub>	0.5 0.1	8.0 10	X 10 <sup>-4</sup>
Small – Signal Current Gain (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 1.0 kHz)	h <sub>fe</sub>	100 100	400 400	-
Output Admittance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 1.0 kHz)	h <sub>oe</sub>	1.0 3.0	40 60	μmhos
Noise Figure (V <sub>CE</sub> = 5.0 Vdc, I <sub>C</sub> = 100 $\mu$ Adc, R <sub>S</sub> = 1.0 k $\Omega$ , f = 1.0 kHz)	NF	-	5.0 4.0	dB

#### SWITCHING CHARACTERISTICS

Delay Time	$(V_{CC} = 3.0 \text{ Vdc}, V_{BE} = -0.5 \text{ Vdc})$	t <sub>d</sub>	-	35	20
Rise Time	$(I_{C} = 10 \text{ mAdc}, I_{B1} = 1.0 \text{ mAdc})$	t <sub>r</sub>	-	35	ns
Storage Time	$(V_{CC} = 3.0 \text{ Vdc}, I_{C} = 10 \text{ mAdc})$	t <sub>s</sub>	-	200	ns
Fall Time	(I <sub>B1</sub> = I <sub>B2</sub> = 1.0 mAdc)	t <sub>f</sub>	-	50	115

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 2. Pulse Test: Pulse Width  $\leq$  300 µs; Duty Cycle  $\leq$  2.0%.

+3 V  $10 < t_1 < 500 \ \mu s$ +3 V o tı DUTY CYCLE = 2% +10.9 V DUTY CYCLE = 2% ≥275 300 ns 🔶 +10.9 V ≥<sub>275</sub> 10 k 10 k 0 ١A -0.5 V 1N916 7  $C_s < 4 \, pF$  $C_s < 4 \text{ pF}^*$ - < 1 ns --9.1 V′ - < 1 ns -

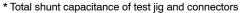
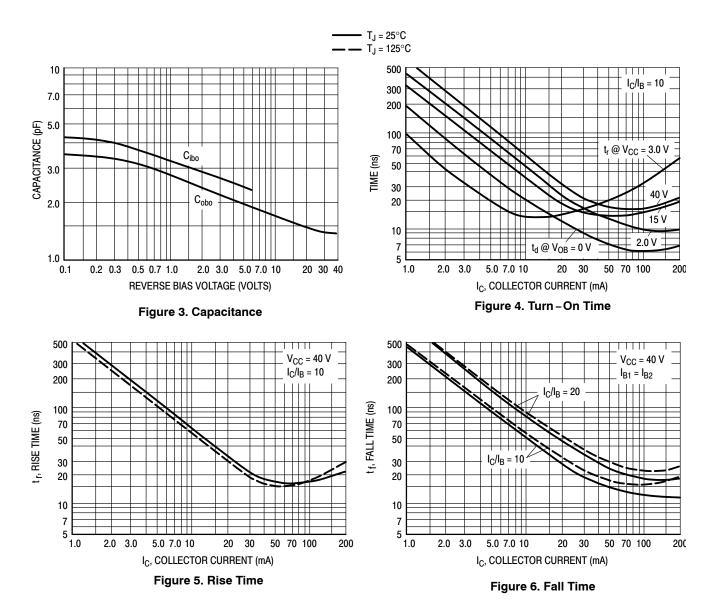


Figure 1. Delay and Rise Time Equivalent Test Circuit

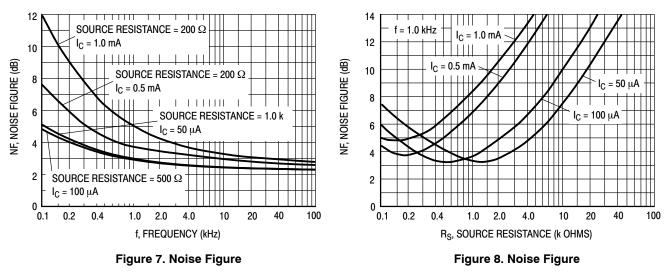
Figure 2. Storage and Fall Time Equivalent Test Circuit

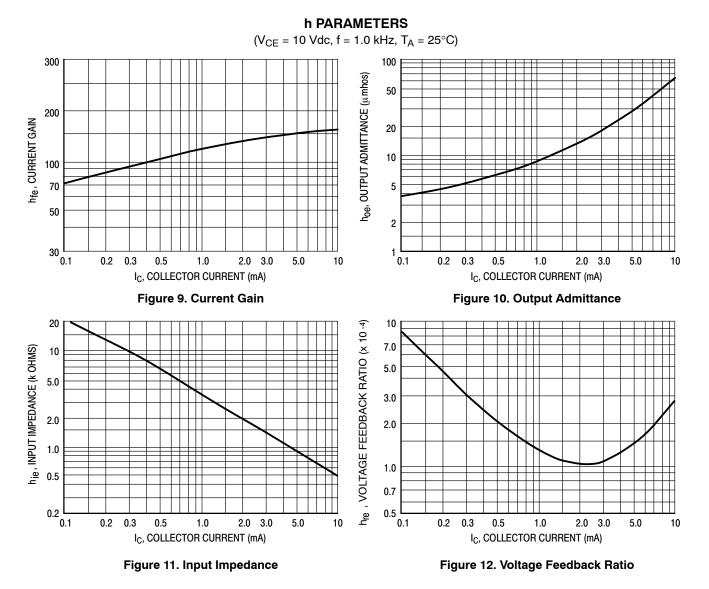
## **TYPICAL TRANSIENT CHARACTERISTICS**





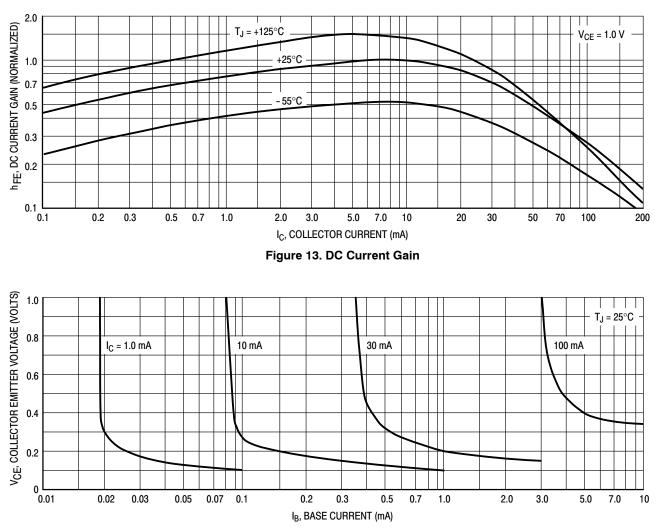
(V<sub>CE</sub> = 5.0 Vdc,  $T_A$  = 25°C, Bandwidth = 1.0 Hz)



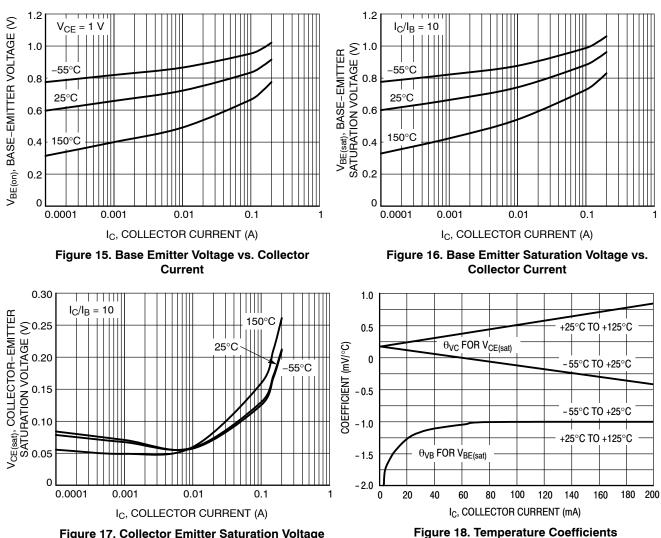


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### **TYPICAL STATIC CHARACTERISTICS**







## TYPICAL STATIC CHARACTERISTICS

Figure 17. Collector Emitter Saturation Voltage vs. Collector Current

#### MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



# ONSEMI

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3. CULLECTOR 2       3. BASE 2       3. ANDDE/ANDDE 2         4. EMITTER 2       4. CATHODE 2       4. CATHODE 2         5. BASE 2       5. BASE 1       5. CATHODE 2         6. COLLECTOR 1       6. COLLECTOR 1       6. ANDDE/ANDDE 1         RECOMMENDED MOUNTING FOOTPRINT*         STYLE 4:         PIN 1. CATHODE       2. CATHODE         2. COLLECTOR 2       3. ANDDE         3. BASE 2       3. ANDDE         3. BASE 2       5. CATHODE         2. COLLECTOR 2       2. CATHODE         3. BASE 3       ANDDE         3. BASE 4       4. ANDDE         4. EMITTER 4       STYLE 6:         9. COLLECTOR 5. CATHODE       3. CATHODE 2         3. BASE 4       4. ANDDE 4         4. CATHODE 5. CATHODE 5. CATHODE 5. CATHODE 6. CATHODE 6. CATHODE 6. CATHODE 7. COLDERING TECHNIQUES REFERENCE MANUAL, SOLDERING TOR SOLDERING TOR SOLDERING AND MOUNTING TECHNICE REVIEW MANUAL, SOLDERING MANUAL, SOLDERING PINCHARMOLES REFERENCE MANUAL, SOLDERING DIAGRAM*         YILE 7:       STYLE 8:       STYLE 9:         9. NAIDE 2       ANDDE 5. GATE 2         3. CATHODE 3. GATE 2       SORAIN 2. GATE 2         3. CATHODE 4. SDURCE 4. SDURCE 2       SORAIN 1         3. CATHODE 5. DRAIN 6. DRAIN 1       SORAIE 2         3. C				E 1			+-	
5. BASE 2       5. BASE 1       5. CATHIDE 2       0.30         6. COLLECTOR 1       6. COLLECTOR 1       6. ANDE/ANDDE 1       RECOMMENDED MOUNTING FOOTPRINT*         STYLE 4:       STYLE 5:       STYLE 6:       PIN 1. CATHIDDE       PIN 1. CATHIDDE         2. COLLECTOR       2. CATHIDDE       2. CATHIDDE       3. BASE       3. ANDDE         3. BASE       3. ANDDE       3. CATHIDDE       3. CATHIDDE       5. CATHIDDE         4. EMITTER       4. ANDDE       4. CATHIDDE       5. CATHIDDE       5. CATHIDDE         5. COLLECTOR       5. CATHIDDE       5. CATHIDDE       5. CATHIDDE       5. CATHIDDE         5. COLLECTOR       6. CATHIDDE       5. CATHIDDE       5. CATHIDDE       5. CATHIDDE         5. COLLECTOR       6. CATHIDDE       6. CATHIDDE       6. CATHIDDE       6. CATHIDDE         2. ANDDE       2. DRAIN       2. GATE 1       2. MANDA       MARKING DIAGRAM*         STYLE 7:       STYLE 8:       STYLE 9:       MARKING DIAGRAM*       MARKING DIAGRAM*         2. ANDDE       2. DRAIN       3. DRAIN 1       2. GATE 2       MARKING DIAGRAM*         3. CATHIDDE       3. DRAIN       5. DIRCE 1       XX = Specific Device Code       M = Month Code         4. CATHIDDE 1       FIN 1. EMITTER 2						$\frac{1}{1}$		
STYLE 4:       STYLE 5:       STYLE 6:       *       FOR ADDITIONAL INFORMATION ON OUR Pb-FREE         PIN 1. CATHIDDE       PIN 1. CATHIDDE       2. CATHIDDE       2. CATHIDDE       3. CATHIDDE         3. BASE       3. ANDDE       3. CATHIDDE       3. CATHIDDE       4. CATHIDDE       4. CATHIDDE         5. COLLECTOR       5. CATHIDDE       4. CATHIDDE       4. CATHIDDE       5. CATHIDDE       5. CATHIDDE         6. COLLECTOR       6. CATHIDDE       5. CATHIDDE       6. CATHIDDE       6. CATHIDDE       6. CATHIDDE         7. CATHIDDE       8. CATHIDDE       6. CATHIDDE       6. CATHIDDE       6. CATHIDDE       6. CATHIDDE         8. ANDDE       9. CATHIDDE       6. CATHIDDE       6. CATHIDDE       6. CATHIDDE       6. CATHIDDE         8. ANDDE       2. DRAIN       2. GATE 1       3. DRAIN 2       6. CATHIDDE       6. CATHIDDE         2. ANDDE       3. DRAIN       5. GATE 2       3. CATHIDE       XX = Specific Device Code         M       M = Month Code       •       = D-Free Package         STYLE 10:       STYLE 11:       FTHIS INFORMATION IS generic. Please refer to         9. N/C       2. BASE 2       3. CATHIDDE       9. COLLECTUR 1         4. ANDDE 1       9. ASEE 1       0. CATHIDE       PD-Free indicat						′ / 		
STYLE 4:       STYLE 5:       STYLE 6:       *       FOR ADDITIONAL INFORMATION ON OUR Pb-FREE         PIN 1. CATHIDDE       2. CATHIDDE       2. ANIDDE       3. BASE       3. ANDDE       3. CATHIDDE         3. BASE       3. ANDDE       3. CATHIDDE       3. CATHIDDE       3. CATHIDDE       3. CATHIDDE         4. EMITTER       4. ANDDE       4. CATHIDDE       5. CATHIDDE       5. CATHIDDE       5. CATHIDDE         5. COLLECTOR       6. CATHIDDE       5. CATHIDDE       5. CATHIDDE       6. CATHIDDE       6. CATHIDDE         6. COLLECTOR       6. CATHIDDE       5. CATHIDDE       6. CATHIDDE       6. CATHIDDE       6. CATHIDDE         3. CATHIDDE       3. CATHIDDE       3. CATHIDDE       6. CATHIDDE       6. CATHIDDE       6. CATHIDDE         3. CATHIDDE       3. GATE       3. DRAIN       2. GATE 1       3. DRAIN 2       4. CATHIDDE       4. SDURCE       2. ANDICE         3. CATHIDDE       4. CATHIDDE       5. GATE 2       3. DRAIN 1       XX = Specific Device Code       M = Month Code       -       = Pb-Free Package         STYLE 10'       STYLE 11'       FIN 1. CATHIDE       2. BASE 2       3. CATHIDDE       *This information is generic. Plase refer to device data sheet for actual part marking.         2. N/C       3. CATHIDDE 2       3. CATH	6. COLLECTOR 1	6. COLLECTOR 1	6. ANDDE/	ANDDE 1				
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4. EMITTER       4. ANDDE       4. CATHEDE       MANUAL, SOLDERRM/D.         5. COLLECTOR       6. CATHEDE       5. CATHEDE       GENERIC         6. COLLECTOR       6. CATHEDE       6. CATHEDE       MARKING DIAGRAM*         STYLE 7:       STYLE 8:       STYLE 9:       MARKING DIAGRAM*         PIN 1. CATHEDE       2. DRAIN       2. GATE       3. DRAIN 2         3. CATHEDE       3. GATE       3. DRAIN 2       1         4. CATHEDE       4. SDURCE       4. SDURCE 2       1         5. ANDDE       5. DRAIN       5. GATE 2       XX = Specific Device Code         M = Month Code       •       = Pb-Free Package         STYLE 10:       STYLE 11:       XX = Specific Device Code         PIN 1. CATHEDE       1. DEMITTER 2       *This information is generic. Please refer to         2. N/C       2. BASE 2       device data sheet for actual part marking.         3. CATHEDE 1       PIN 1. EMITTER 1       or may not be present. Some products may         3. CATHEDE 2       4. EMITTER 1       or may not be present. Some products may         3. N/C       5. BASE 1       or may not be present. Some products may         4. ANDDE 2       4. EMITTER 1       or may not be present. Some products may         5. N/C       5. BASE 1	3. BASE	3. ANDDE	3. CATHOD	Ł	OWNLOAD THE	ON SEMIC	ONDUCTOR	SOLDERING
GENERIC MARKING DIAGRAM*         STYLE 7:       STYLE 8:       STYLE 9:         PIN 1. CATHODE       PIN 1. DRAIN       PIN 1. SDURCE 1         2. ANDDE       2. DRAIN       2. GATE 1         3. CATHODE       3. GATE       3. DRAIN 2         4. CATHODE       4. SDURCE       4. SDURCE 4. SDURCE 2         5. ANDDE       5. DRAIN       5. GATE 2         6. CATHODE       6. DRAIN       6. DRAIN 1         7       File       * Style 10:         8. STYLE 10:       STYLE 11:       * = Pb-Free Package         STYLE 10:       STYLE 11:         PIN 1. CATHODE 1       PIN 1. EMITTER 2         2. N/C       2. BASE 2         3. CATHODE 2       3. COLLECTOR 1         9. N/C       3. CALLECTOR 1         4. ANDDE 2       4. EMITTER 1         5. N/C       SASE 1         6. ANDDE 1       6. COLLECTOR 2         9. N/C       SASE 1         6. ANDDE 1       6. COLLECTOR 2         9. N/C       SASE 1         6. ANDDE 1       6. COLLECTOR 2         9. N/C       SASE 1         6. ANDDE 1       6. COLLECTOR 2         9. ONTROLECTOR 2       NOT 5. BASE 1         6. A	5. COLLECTOR	5. CATHODE	5. CATHOD	E				
STYLE 7:       STYLE 8:       STYLE 9:       MARKING DIAGRAM*         PIN 1. DRAIN       PIN 1. SDURCE 1      GATE 1      GATE 1         2. ANDDE       2. DRAIN       2. GATE 1      GATE 2         3. CATHDDE       3. GATE       3. DRAIN 2      GATE 2         4. CATHDDE       4. SDURCE 4. SDURCE 2       1      GATE 2         5. ANDDE       5. GATE 2      GATE 2       1         6. CATHDDE       6. DRAIN       6. DRAIN 1       XX = Specific Device Code         M       = Month Code       -       -       = Pb-Free Package         STYLE 10:       STYLE 11:       *This information is generic. Please refer to         2. N/C       2. BASE 2       device data sheet for actual part marking.         3. CATHDDE 2       3. COLLECTOR 1       Pb-Free indicator, "G" or microdot "•", may         4. ANDDE 2       3. COLLECTOR 1       Pb-Free indicator, "G" or microdot "•", may         5. N/C       5. BASE 1       or may not be present. Some products may         6. ANDDE 1       6. COLLECTOR 2       Electronic versions are uncontrolled except when accessed directly from the Document Repository.         Printed versions are uncontrolled except when stamped "CONTROLLED" in red.       Electronic versions are uncontrolled except when stamped "CONTROLLED"	6. COLLECTOR	6. CATHODE	6. CATHOD	E		GENER	C	
PIN 1. CATHIDE       PIN 1. DRAIN       PIN 1. SDURCE 1         2. ANDDE       2. DRAIN       2. GATE 1         3. CATHIDDE       3. GATE       3. DRAIN 2         4. CATHIDDE       4. SDURCE       4. SDURCE 2         5. ANDDE       5. DRAIN       5. GATE 2         6. CATHIDDE       6. DRAIN       6. DRAIN         7       6. DRAIN       6. DRAIN         8. CATHIDDE       6. DRAIN       6. DRAIN         9. OLATHIDE       6. DRAIN       6. DRAIN         9. CATHIDDE       6. DRAIN       6. DRAIN         9. CATHIDDE       1. EMITTER 2       XX = Specific Device Code         M       = Month Code       =         9. N/C       2. BASE 2       device data sheet for actual part marking.         3. CATHIDDE 2       3. COLLECTOR 1       Pb-Free indicator, "G" or microdot "•", may         4. ANDDE 2       4. EMITTER 1       or may not be present. Some products may         5. N/C       5. BASE 1       or may not be present. Some products may         6. ANDDE 1       6. COLLECTOR 2       Electronic versions are uncontrolled except when accessed directly from the Document Repository.         Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th>								
3. CATHIDE 4. CATHIDE 5. ANDDE 5. ANDDE 6. DRAIN 5. GATE 2 5. ANDDE 6. DRAIN 6. DRAIN 5. GATE 2 5. ANDDE 6. DRAIN 6. DRAIN 7. STYLE 10: 7. N/C 7. SASE 2 7. CATHIDE 7. N/C 7. SASE 2 7. CATHIDE 7. N/C 7. SASE 2 7. CATHIDE 7. N/C 7. SASE 2 7. CATHIDE 7. N/C 7. SASE 1 7. COLLECTOR 7.	PIN 1. CATHODE	PIN 1. DRAIN	PIN 1. SOURCE	1			1	
5. ANDDE       5. DRAIN       5. GATE 2         6. CATHEDDE       6. DRAIN       6. DRAIN 1         XX       = Specific Device Code         M       = Month Code         •       = Pb-Free Package         STYLE 10:       STYLE 11:         PIN 1. CATHEDDE 1       PIN 1. EMITTER 2         2. N/C       2. BASE 2         3. CATHEDDE 2       3. COLLECTER 1         4. ANEDDE 2       4. EMITTER 1         5. N/C       5. BASE 1         6. ANEDE 1       6. COLLECTER 2         Electronic versions are uncontrolled except when accessed directly from the Document Repository.         Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	3. CATHODE	3. GATE	3. DRAIN â			XXM•		
STYLE 10:       STYLE 11: <ul> <li>PD-Free Package</li> <li>*This information is generic. Please refer to</li> <li>ACTHDDE 1</li> <li>PIN 1. EMITTER 2</li> <li>CATHDDE 2</li> <li>CDLLECTOR 1</li> <li>PD-Free indicator, "G" or microdot "=", may</li> <li>or may not be present. Some products may</li> <li>N/C</li> <li>SBASE 1</li> <li>COLLECTOR 2</li> </ul> <li>DOCUMENT NUMBER:</li> <li>98AON11126D</li> <li>Electronic versions are uncontrolled except when accessed directly from the Document Repository.</li> <li>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.</li>							]	
STYLE 10:       STYLE 11:       - = Pb-Free Package         PIN 1. CATHIDE 1       PIN 1. EMITTER 2       *This information is generic. Please refer to         2. N/C       2. BASE 2       device data sheet for actual part marking.         3. CATHIDE 2       3. COLLECTOR 1       Pb-Free indicator, "G" or microdot "=", may         4. ANDDE 2       4. EMITTER 1       or may not be present. Some products may         5. N/C       5. BASE 1       ort follow the Generic Marking.         6. ANDDE 1       6. COLLECTOR 2       Electronic versions are uncontrolled except when accessed directly from the Document Repository.         Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	6. CATHODE	6. DRAIN	6. DRAIN 1	L		•		
PIN 1. CATHIDE 1       PIN 1. EMITTER 2       *This information is generic. Please refer to device data sheet for actual part marking.         2. N/C       2. BASE 2       device data sheet for actual part marking.         3. CATHIDE 2       3. CDLLECTOR 1       Pb-Free indicator, "G" or microdot "•", may         4. ANDDE 2       4. EMITTER 1       or may not be present. Some products may         5. N/C       5. BASE 1       not follow the Generic Marking.         6. ANDDE 1       6. CDLLECTOR 2       Electronic versions are uncontrolled except when accessed directly from the Document Repository.         Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.       Image: Control of the co								
3. CATHEDE 2       3. CELLECTER 1       Pb-Free indicator, "G" or microdot "•", may         4. ANDDE 2       4. EMITTER 1       or may not be present. Some products may         5. N/C       5. BASE 1       not follow the Generic Marking.         6. ANDDE 1       6. CELLECTER 2       not follow the Generic Marking.         DOCUMENT NUMBER:       98AON11126D       Electronic versions are uncontrolled except when accessed directly from the Document Repository.	PIN 1. CATHODE 1	PIN 1. EMITTER 2				ų.		
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6. ANDDE 1       6. CDLLECTOR 2       not follow the Generic Marking.         DOCUMENT NUMBER:       98AON11126D       Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.					or may not be p	resent. So	me product	
Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	6. ANDDE 1	6. COLLECTOR 2						he Document Repository.
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