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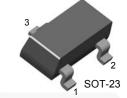


December 2013

# MMBT3646 NPN Switching Transistor

## **Features**

- · NPN High Speed Switching Transistor
- · Process 22



1. Base 2. Emitter 3. Collector

## **Ordering Information**

Part Number	Top Mark	Package	Packing Method	
MMBT3646	23	SOT-23 3L	Tape and Reel	

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	15	V
$V_{CES}$	Collector-Emitter Voltage	40	V
$V_{CBO}$	Collector-Base Voltage	40	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current (DC) - Continuous	300	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C

## Thermal Characteristics(1)

Values are at  $T_C$  = 25°C unless otherwise noted.

Symbol	Parameter	Value	Unit
P <sub>D</sub>	Total Device Dissipation at T <sub>A</sub> = 25°C	625	mW
	Derate Above 25°C	5	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	°C/W

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### Note:

1. PCB size: FR-4 trace width is 50 mil / 25 mil /15 mil, thickness: 2 OZ, minimum land pattern size.

## **Electrical Characteristics**

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 100 μA, V <sub>BE</sub> = 0	40			V
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage <sup>(2)</sup>	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	15			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 100 \mu\text{A}, I_E = 0$	40			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 100 \mu A, I_C = 0$	5			V
		$V_{CE} = 20 \text{ V}, V_{BE} = 0$			0.5	μΑ
I <sub>CES</sub>	Collector Cut-Off Current	$V_{CE} = 20 \text{ V}, V_{BE} = 0,$ $T_A = 65^{\circ}\text{C}$			3.0	
		$V_{CE} = 0.4 \text{ V}, I_{C} = 30 \text{ mA}$	30		120	
h <sub>FE</sub>	DC Current Gain <sup>(2)</sup>	$V_{CE} = 0.5 \text{ V}, I_{C} = 100 \text{ mA}$	25			
		$V_{CE} = 1.0 \text{ V}, I_{C} = 300 \text{ mA}$	15			
		$I_C = 30 \text{ mA}, I_B = 3 \text{ mA}$			0.20	V
	Collector-Emitter Saturation Voltage <sup>(2)</sup>	I <sub>C</sub> = 100 mA, I <sub>B</sub> = 10 mA			0.28	
V <sub>CE</sub> (sat)		I <sub>C</sub> = 300 mA, I <sub>B</sub> = 30 mA			0.50	
		$I_{C} = 30 \text{ mA}, I_{B} = 3 \text{ mA},$ $T_{A} = 65^{\circ}\text{C}$			0.30	
	Base-Emitter Saturation Voltage <sup>(2)</sup>	$I_C = 30 \text{ mA}, I_B = 3 \text{ mA}$	0.73		0.95	V
V <sub>BE</sub> (sat)		I <sub>C</sub> = 100 mA, I <sub>B</sub> = 10 mA			1.20	
		I <sub>C</sub> = 300 mA, I <sub>B</sub> = 30 mA			1.70	
C <sub>ob</sub>	Output Capacitance	$V_{CE} = 5 \text{ V}, I_{E} = 0,$ f = 1MHz			5	pF
C <sub>ib</sub>	Input Capacitance	$V_{EB} = 0.5 \text{ V}, I_{C} = 0,$ f = 1MHz			8	pF
t <sub>on</sub>	Turn-On Time	$V_{CC} = 10 \text{ V}, I_{C} = 300 \text{ mA},$			18	ns
t <sub>d</sub>	Delay Time	I <sub>B1</sub> = 30 mA,			10	ns
t <sub>r</sub>	Rise Time	V <sub>CE</sub> (off) = 3 V			15	ns
t <sub>off</sub>	Turn-Off Time	40.771 000 1	-/		28	ns
t <sub>f</sub>	Fall Time	$V_{CC} = 10 \text{ V, } I_{C} = 300 \text{ mA,}$ $I_{B1} = I_{B2} = 30 \text{ mA}$			15	ns
t <sub>s</sub>	Storage Time	iR1 iR5 00 illut			20	ns

## Note:

2. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%.

## **Physical Dimensions**

## SOT-23

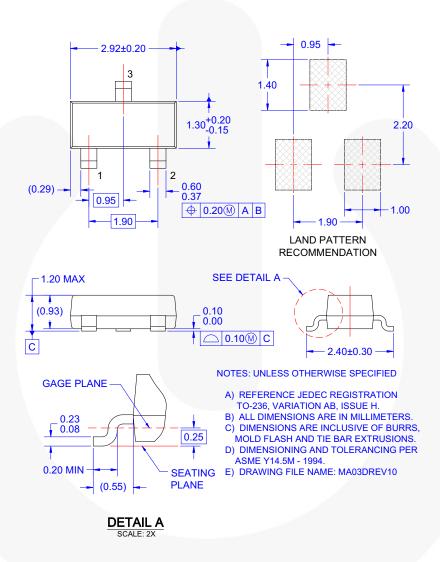


Figure 1. 3-LEAD, SOT-23, JEDEC TO-236, LOW PROFILE (ACTIVE)

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Definition of Terms			
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
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