

Switch Mode Power Rectifiers MBR8H100MFS, NRVB8H100MFS

Features

- Low Power Loss / High Efficiency
- New Package Provides Capability of Inspection and Probe After Board Mounting
- Guardring for Stress Protection
- Low Forward Voltage Drop
- 175°C Operating Junction Temperature
- WF Suffix for Products with Wettable Flanks
- NRVB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free Devices

Mechanical Characteristics:

- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94-0 @ 0.125 in.
- Lead Finish: 100% Matte Sn (Tin)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL 1 Requirements

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	100	V
Average Rectified Forward Current (Rated V _R , T _C = 165°C)	I _{F(AV)}	8.0	Α
Peak Repetitive Forward Current, (Rated V _R , Square Wave, 20 kHz, T _C = 162°C)	I _{FRM}	16	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	75	A
Storage Temperature Range	T _{stg}	-65 to +175	°C
Operating Junction Temperature	T _J	-55 to +175	°C
Unclamped Inductive Switching Energy (10 mH Inductor, Non-repetitive)	E _{AS}	75	mJ
ESD Rating (Human Body Model)		3B	
ESD Rating (Machine Model)		M4	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

NOTE: The heat generated must be less than the thermal conductivity from Junction-to-Ambient: dPD/dTJ < 1/RJA

1

SCHOTTKY BARRIER RECTIFIERS 8 AMPERES 100 VOLTS

1,2,3 0 5,6

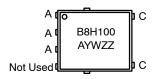






(FULL-CUT SO8FL WF) CASE 507BA DFNW5

MARKING DIAGRAM



B8H100 = Specific Device Code A = Assembly Location

Y = Year
W = Work Week
ZZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping [†]
MBR8H100MFST1G	SO-8 FL (Pb-Free)	1500 / Tape & Reel
MBR8H100MFST3G	SO-8 FL (Pb-Free)	5000 / Tape & Reel
NRVB8H100MFST1G	SO-8 FL (Pb-Free)	1500 / Tape & Reel
NRVB8H100MFST3G	SO-8 FL (Pb-Free)	5000 / Tape & Reel
NRVB8H100MFSWFT1G	SO-8 FL (Pb-Free)	1500 / Tape & Reel
NRVB8H100MFSWFT3G	SO-8 FL (Pb-Free)	5000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MBR8H100MFS, NRVB8H100MFS

THERMAL CHARACTERISTICS

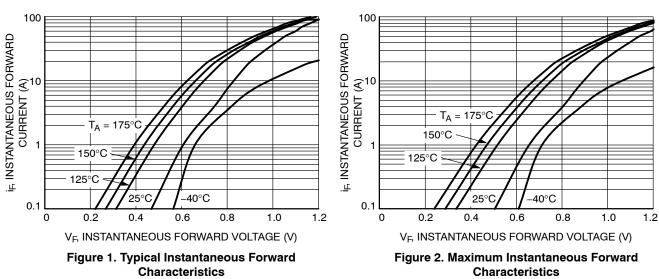
Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance, Junction-to-Case, Steady State (Assumes 600 mm² 1 oz. copper bond pad, on a FR4 board) (Note 2)	$R_{ heta JC}$	-	2.2	°C/W
Thermal Resistance, Junction-to-Ambient, Steady State (Note 2)	$R_{\theta JA}$	-	53.1	°C/W

ELECTRICAL CHARACTERISTICS

Instantaneous Forward Voltage (Note 1)	٧ _F			V
(i _F = 8 Amps, T _J = 125°C)		0.68	0.76	
(i _F = 8 Amps, T _J = 25°C)		0.81	0.90	
Instantaneous Reverse Current (Note 1)	i _R			μΑ
(Rated dc Voltage, T _J = 125°C)		180	300	
(Rated dc Voltage, T _J = 25°C)		0.06	2	

- 1. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.
- 2. Surface-mounted on FR4 board using a 650 mm², 1 oz. Cu pad.

TYPICAL CHARACTERISTICS



(¥) 1.E+00 1.E-01 1.E-02 0 1.E-03 € 1.E+00 CURRENT 1.E-01 1.E-02 T_A = 175°C T_A = 175°C ▮ 1.E-03 1.E-04 SH 1.E-04 H 1.E-05 H 1.E-06 150°C 1.E-04 $T_A = 125^{\circ}C$ $T_{\Delta} = 125^{\circ}C$ 1.E-05 1.E-09 1.E-09 1.E-10 1.E-11 1.E-11 1.E-06 1.E-07 1.E-08 1.E-09 1.E-10 1.E-11 1.E-06 $T_A = 25^{\circ}C$ $T_A = 25^{\circ}C$ $T_A =$ 90 100 50 40 50 60 0 40 60 70 Ě ĉ V_R, INSTANTANEOUS REVERSE VOLTAGE (V) V_R, INSTANTANEOUS REVERSE VOLTAGE (V)

Figure 3. Typical Reverse Characteristics

Figure 4. Maximum Reverse Characteristics

MBR8H100MFS, NRVB8H100MFS

TYPICAL CHARACTERISTICS

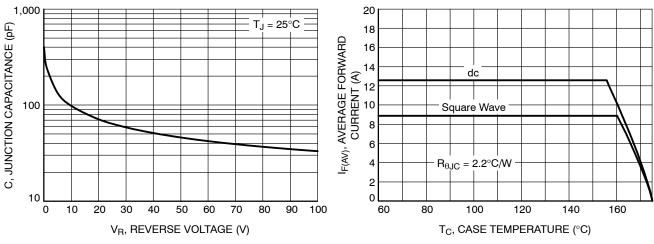


Figure 5. Typical Junction Capacitance

Figure 6. Current Derating

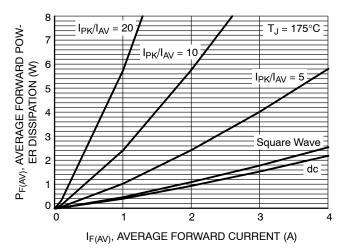


Figure 7. Forward Power Dissipation

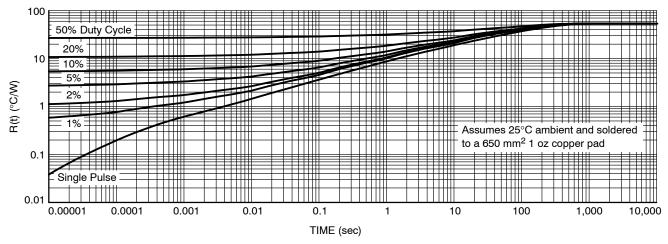


Figure 8. Thermal Response





0.10

SIDE VIEW

DFN5 5x6, 1.27P (SO-8FL) CASE 488AA ISSUE N

DATE 25 JUN 2018

NOTES:

- DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETER. DIMENSION D1 AND E1 DO NOT INCLUDE
- MOLD FLASH PROTRUSIONS OR GATE BURRS

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.90	1.00	1.10
A1	0.00		0.05
b	0.33	0.41	0.51
С	0.23	0.28	0.33
D	5.00	5.15	5.30
D1	4.70	4.90	5.10
D2	3.80	4.00	4.20
E	6.00	6.15	6.30
E1	5.70	5.90	6.10
E2	3.45	3.65	3.85
е	1.27 BSC		
G	0.51	0.575	0.71
K	1.20	1.35	1.50
L	0.51	0.575	0.71
L1	0.125 REF		
M	3.00	3.40	3.80
θ	0 °		12 °

GENERIC MARKING DIAGRAM*



XXXXXX = Specific Device Code

= Lot Traceability

= Assembly Location Α

Υ = Year W = Work Week

ZZ

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ", may or may not be present. Some products may not follow the Generic Marking.





DETAIL A

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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IDENTIFIER





CASE 507BA **ISSUE A**



MILLIMETERS

N□M.

0.575

0.575

0.150 REF

1.35

MAX. 1.10 0.05 0.51

0.33

5.30 5.10

4.20

6.30 6.10

3.85

0.71

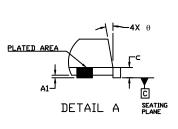
1.50

0.71



DIM

DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
CONTROLLING DIMENSION: MILLIMETERS
DIMENSIONS DI AND EI DO NOT INCLUDE MOLD FLASH,
PROTRUSIONS, OR GATE BURRS.
THIS PACKAGE CONTAINS WETTABLE FLANK DESIGN
FEATURES TO AID IN FILLET FORMATION ON THE LEADS DURING MOUNTING.



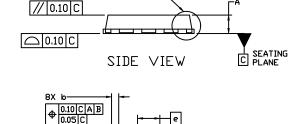
Α	0.90	1.00
A1	0.00	
b	0.33	0.41
С	0.23	0.28
D	5.00	5.15
D1	4.70	4.90
D2	3.80	4.00
Е	6.00	6.15
E1	5.70	5.90
E2	3.45	3.65
е		1.27 BSC

0.51

1.20

0.51

MIN



e/2

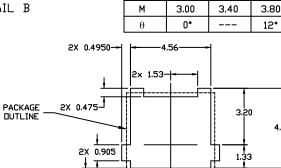
BOTTOM VIEW

-DETAIL B

DETAIL A

TOP VIEW





0.965

4X 1.00-

G

Κ

L1

GENERIC MARKING DIAGRAM*

PIN 5 (EXPOSED PAD)



= Assembly Location Α

Υ = Year W

ZZ

= Work Week = Lot Traceability

XXXXXX = Specific Device Code *This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " =", may or may not be present. Some products

may not follow the Generic Marking.

RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the $\square N$ Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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4X 0.75

DESCRIPTION: DFNW5 5x6 (FULL-CUT SO8FL WF) **PAGE 1 OF 1**

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