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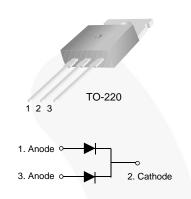


July 2014

# MBR20150CT Dual High Voltage Schottky Rectifier

### **Features**

- · Low Forward Voltage Drop
- · Low Power Loss and High Efficiency
- · High Surge Capability
- RoHS Compliant
- · Matte Tin (Sn) Lead Finish
- Terminal Leads Surface is Corrosion Resistant and able to Withstand to 260°C
- Wave Soldering or per MIL-STD-750 Method 2026.
- · Dual common Cathode



## **Ordering Information**

Part Number	Part Number Top Mark		Packing Method	
MBR20150CTTU	MBR20150CT	TO-220 3L	Rail	

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit	
$V_{RRM}$	Maximum Repetitive Reverse Voltage		150	V
V <sub>R</sub>	Maximum DC Reverse Voltage		150	V
I <sub>F(AV)</sub>	Average Rectified Forward Current, at T <sub>C</sub> = 120°C	per Leg	10	Α
		per Device	20	
I <sub>FSM</sub>	Peak Forward Surge Current, 8.3 ms Half-Sine Wave		150	Α
T <sub>STG</sub>	Storage Temperature Range		-50 to +150	°C
TJ	Operating Junction Temperature		150	°C

## Thermal Characteristics(1)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case per Leg	1.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient per Leg	62.5	°C/W

### Note:

1. MIL standard 883-1012 and JESD51-10.

## Electrical Characteristics(2)

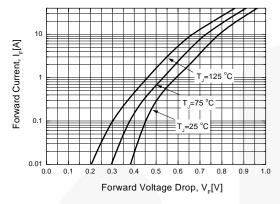
Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 150 V, T <sub>C</sub> = 25°C		0.2	mA
		$V_R = 150 \text{ V}, T_C = 125^{\circ}\text{C}$		2.0	
V <sub>F</sub>	Forward Voltage	$I_F = 10 \text{ A}, T_C = 25^{\circ}\text{C}$		0.85	V
		I <sub>F</sub> = 10 A, T <sub>C</sub> = 125°C		0.75	
		$I_F = 20 \text{ A}, T_C = 25^{\circ}\text{C}$		0.95	
		$I_F = 20 \text{ A}, T_C = 125^{\circ}\text{C}$		0.85	

## Note:

2. DC Item are tested by pulse test: pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2\%.$ 

## **Typical Performance Characteristics**



**Figure 1. Forward Current Characteristics** 

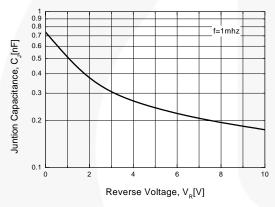


Figure 3. Junction Capacitance

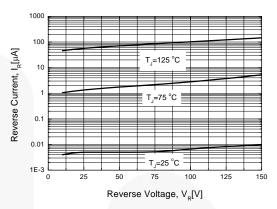


Figure 2. Reverse Leakage Current

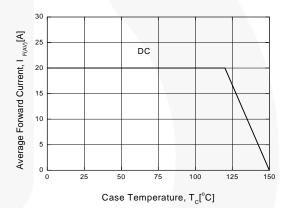


Figure 4. Power Derating

## **Physical Dimensions** SUPPLIER "B" PACKAGE Ø4.00 3.50 10.67 SUPPLIER "A" PACKAGE SHAPE 3.40 2.50 IF PRESENT, SEE NOTE 'D' A 16.51 15.42 8.13 [2.46] С 14.04 12.70 FRONT VIEWS .62 42 ── "A1" SEE NOTE "F" OPTIONAL CHAMFER A 14.30 11.50 NOTE "I" BOTTOM VIEW NOTES: A) REFERENCE JEDEC, TO-220, VARIATION AB B) ALL DIMENSIONS ARE IN MILLIMETERS. DIMENSIONS ARE IN MILLIME IERS. DIMENSIONS COMMON TO ALL PACKAGE SUPPLIERS EXCEPT WHERE NOTED [ ]. LOCATION OF MOLDED FEATURE MAY VARY (LOWER LEFT CORNER, LOWER CENTER) 2 AND CENTER OF THE PACKAGE) DOES NOT COMPLY JEDEC STANDARD VALUE. "A1" DIMENSIONS AS BELOW: SINGLE GAUGE = 0.51 - 0.61 DUAL GAUGE = 1.10 - 1.45 DRAWING FILE NAME: TO220B03REV8 PRESENCE IS SUPPLIER DEPENDENT SUPPLIER DEPENDENT MOLD LOCKING HOLES IN HEATSINK J) FAIRCHILD SEMICONDUCTOR **BACK VIEW** SIDE VIEW

Figure 5. TO-220, MOLDED, 3LEAD, JEDEC VARIATION AB

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Definition of Terms			
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.	
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