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August 2017

ON Semiconductor

# ISL9K460P3 8 A, 600 V, STEALTH™ II Diode

#### **Features**

- Stealth Recovery  $t_{rr}$  = 17 ns (@  $I_F$  = 4 A)
- Max Forward Voltage, V<sub>F</sub> = 2.4 V (@ T<sub>C</sub> = 25°C)
- 600 V Reverse Voltage and High Reliability
- · Avalanche Energy Rated
- · RoHS Compliant

#### **Applications**

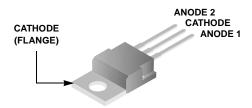
- SMPS FWD
- · Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- Snubber Diode

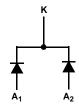
#### Description

The ISL9K460P3 is a STEALTH $^{\text{TM}}$  dual diode optimized for low loss performance in high frequency hard switched applications. The STEALTH $^{\text{TM}}$  family exhibits low reverse recovery current ( $I_{rr}$ ) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low  $I_{rr}$  and short ta phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTH $^{\text{TM}}$  diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

## Package Symbol

#### **JEDEC TO-220AB**



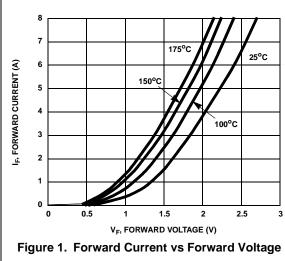


### Device Maximum Ratings (per leg) T<sub>C</sub>= 25°C unless otherwise noted

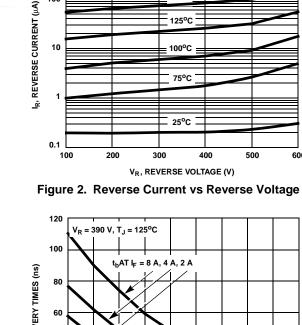
Symbol	Parameter	Rating	Unit	
$V_{RRM}$	Peak Repetitive Reverse Voltage	600		
$V_{RWM}$	Working Peak Reverse Voltage	600	V	
V <sub>R</sub>	DC Blocking Voltage	600	V	
I <sub>F(AV)</sub>	Average Rectified Forward Current (T <sub>C</sub> = 155°C)	4	Α	
` ,	Total Device Current (Both Legs)	8	Α	
I <sub>FRM</sub>	Repetitive Peak Surge Current (20kHz Square Wave)	8	Α	
I <sub>FSM</sub>	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	50	Α	
P <sub>D</sub>	Power Dissipation	58	W	
E <sub>AVL</sub>	Avalanche Energy (0.5A, 80mH)	10	mJ	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to 175	°C	
TL	Maximum Temperature for Soldering	300	°C	
$T_{PKG}^{-}$	Leads at 0.063in (1.6mm) from Case for 10s	260	°C	
	Package Body for 10s, See Techbrief TB334			

CAUTION: Stresses above those listed in "Device Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Part Num	ber Top Mark	Package	Packing Method	Reel Size	Tape Width		Quantity	
ISL9K460F	P3 K460P3	TO-220	Tube	N/A	N/A		50	
Electric	cal Characteris	stics (per leg	T <sub>C</sub> = 25°C unless oth	nerwise noted			•	
Symbol	Param	neter	Test Cond	ditions	Min Typ		Max	Unit
Off State	Characteristics		•		•			
I <sub>R</sub>	Instantaneous Reverse Current		V <sub>R</sub> = 600 V	T <sub>C</sub> = 25°C	T -	_	100	μА
				T <sub>C</sub> = 125°C	-	-	1.0	mA
On State	Characteristics		<u>.</u>		•			
V <sub>F</sub>	Instantaneous Forward Voltage		I <sub>F</sub> = 4 A	T <sub>C</sub> = 25°C	_	2.0	2.4	V
				T <sub>C</sub> = 125°C	-	1.6	2.0	V
Dynamic <sub>C</sub> J	Characteristics  Junction Capacitance	<u> </u>	V <sub>R</sub> = 10 V, I <sub>F</sub> = 0 A		l <u>-</u>	19	_	pF
- CJ	Junction Capacitance	7	VR - 10 V, IF - 0 A			13		рі
Switchin	g Characteristics	5						
t <sub>rr</sub>	Reverse Recovery Time			$I_F = 1 \text{ A, } di_F/dt = 100 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		17	20	ns
				$I_F = 4 \text{ A}, \text{ di}_F/\text{dt} = 100 \text{ A/}\mu\text{s}, \text{ V}_R = 30 \text{ V}$		19	22	ns
t <sub>rr</sub>	Reverse Recovery Time I <sub>F</sub> = 4 A,		0001/	-	17	-	ns	
I <sub>rr</sub>	Reverse Recovery C	urrent	di <sub>F</sub> /dt = 200 A/μs, V <sub>R</sub> = 390 V,		-	2.6	-	Α
$Q_{rr}$	Reverse Recovery C	harge	T <sub>C</sub> = 25°C		-	22	ı	nC
t <sub>rr</sub>	Reverse Recovery Ti	me	I <sub>F</sub> = 4 A,		-	77	•	ns
S	Softness Factor (t <sub>b</sub> /t <sub>a</sub>	)	di <sub>F</sub> /dt = 200 A/μs,		-	4.2	•	
I <sub>rr</sub>	Reverse Recovery C	urrent	V <sub>R</sub> = 390 V,	V <sub>R</sub> = 390 V, -T <sub>C</sub> = 125°C		2.8	ı	Α
$Q_{rr}$	Reverse Recovery C	harge	10 120 0		-	100	•	nC
t <sub>rr</sub>	Reverse Recovery Ti	me	$I_F = 4 A$		-	54	•	ns
S	Softness Factor (t <sub>b</sub> /t <sub>a</sub>	)	$di_F/dt = 400 \text{ A/}\mu\text{s},$		-	3.5	•	
I <sub>rr</sub>	Reverse Recovery C	urrent	$V_R = 390 \text{ V},$ $T_C = 125^{\circ}\text{C}$		-	4.3	-	Α
Q <sub>rr</sub>	Reverse Recovery C	harge	10 - 123 0			110	-	nC
dI <sub>M</sub> /dt	Maximum di/dt during	y t <sub>b</sub>			-	500	-	A/µs
Thermal	Characteristics							
	I						2.0	°C/M
$R_{\theta JC}$	Thermal Resistance	Junction to Case			-	-	2.6	°C/W



**Typical Performance Curves** 



600

100

10

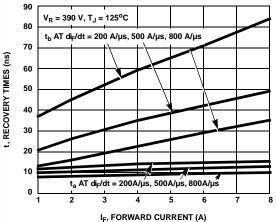
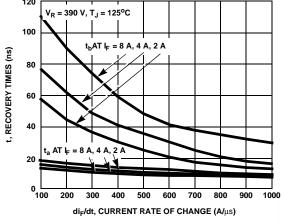


Figure 3.  $t_a$  and  $t_b$  Curves vs Forward Current



175°C

150°C

125°C

100°C

Figure 4. t<sub>a</sub> and t<sub>b</sub> Curves vs di<sub>F</sub>/dt

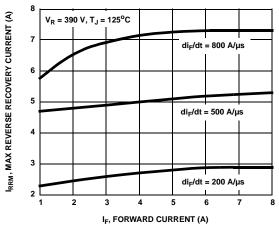


Figure 5. Maximum Reverse Recovery Current vs Forward Current

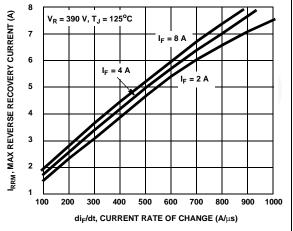
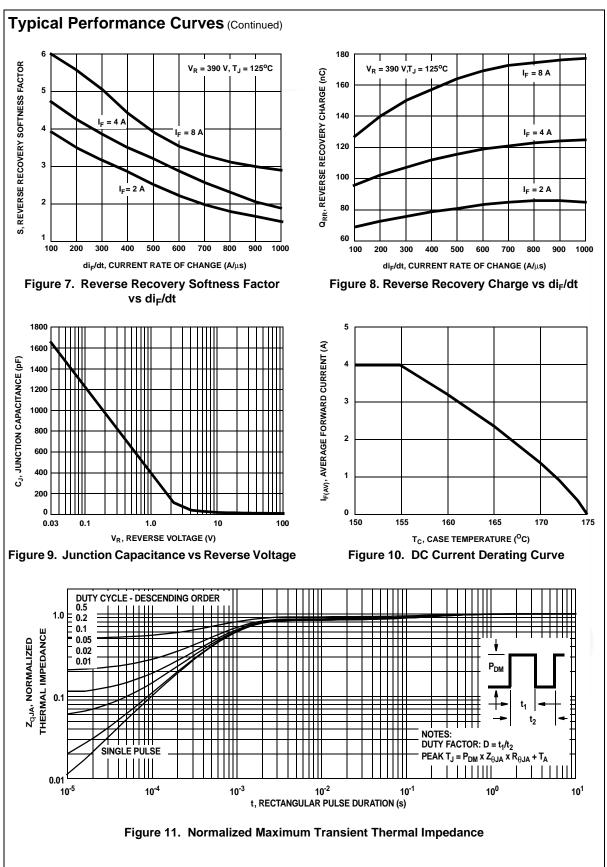
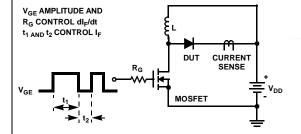


Figure 6. Maximum Reverse Recovery Current vs di<sub>F</sub>/dt



### Test Circuit and Waveforms



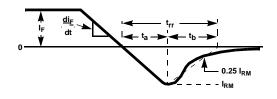


Figure 12.  $t_{rr}$  Test Circuit

Figure 13.  $t_{rr}$  Waveforms and Definitions

```
I = 0.5A

L = 80mH

R < 0.1Ω

V<sub>DD</sub> = 200V

E<sub>AVL</sub> = 1/2L1<sup>2</sup> [V<sub>R(AVL)</sub>/(V<sub>R(AVL)</sub> - V<sub>DD</sub>)]

Q<sub>1</sub> = IGBT (BV<sub>CES</sub> > DUT V<sub>R(AVL)</sub>)

L R

CURRENT

SENSE V<sub>DD</sub>

V<sub>DD</sub>
```

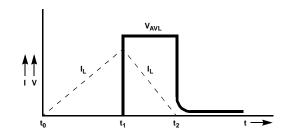


Figure 14. Avalanche Energy Test Circuit

Figure 15. Avalanche Current and Voltage Waveforms

#### **Mechanical Dimensions**

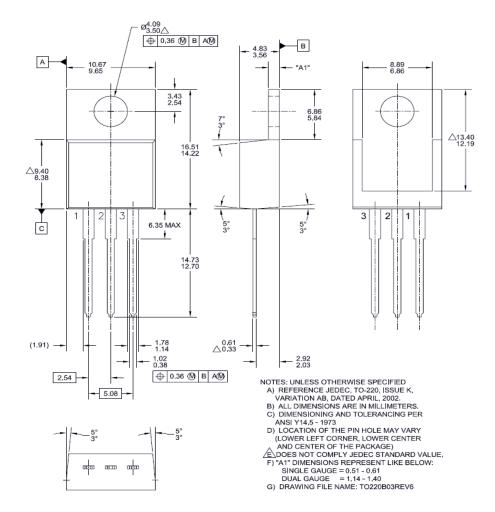


Figure 16. TO-220 3L - TO-220, MOLDED, 3LEAD, JEDEC VARIATION AB

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