



## Product Overview

### PACDN042: Transient Voltage Suppressor Arrays

For complete documentation, see the data sheet

#### Product Description

The PACDN042/43/44/45/46 family of transient voltage suppressor arrays provide a very high level of protection for sensitive electronic components that may be subjected to electrostatic discharge (ESD). The PACDN042/43/44/45/46 devices safely dissipate ESD strikes, exceeding the IEC 61000-4-2 International Standard, Level 4 ( $\pm 8$  kV contact discharge). All pins are rated to withstand  $\pm 20$  kV ESD pulses using the IEC 61000-4-2 contact discharge method. Using the MIL-STD-883D (Method 3015 specification for Human Body Model (HBM) ESD, all pins are protected from contact discharges of greater than  $\pm 30$  kV.

#### Features

- Two, three, four, five, or six transient voltage suppressors
- Compact SMT package saves board space and facilitates layout in space critical applications
- In-system ESD protection to  $\pm 20$  kV contact discharge, per the IEC 61000-4-2 international standard

#### Applications

- ESD protection of PC ports, including USB ports, serial ports, parallel ports, IEEE1394 ports, docking ports, proprietary ports, etc.
- Protection of interface ports or IC pins which are exposed to high ESD levels

#### End Products

- Digital TV, Set Top Box, PC/Notebook, Gaming

#### Part Electrical Specifications

Product	Compliance	Status	Interface	Number of Lines	Direction	C Max (pF)	$V_{(BR)}$ Min (V)	$V_{RWM}$ Max (V)	$I_R$ Max (uA)	$P_{PK}$ Max (W)	Package Type
PACDN042Y3R	AEC Qualified Pb-free Halide free	Active	USB 2.0	2	Unidirectional	30	6.2	5	0.1	0.225	SOT-23-3
SZPACDN042Y3R	AEC Qualified PPAP Capable Pb-free Halide free	Active	USB 2.0	2	Unidirectional	30	6.2	5	0.1	0.225	SOT-23-3

For more information please contact your local sales support at [www.onsemi.com](http://www.onsemi.com)

Created on: 7/11/2015