

Product Overview

P3PS850BH: Timing-Safe™ Peak EMI Reduction IC

For complete documentation, see the data sheet.

P3PS850BH is a versatile Timing-Safe Peak EMI reduction IC. P3PS850BH accepts one input from an external reference, and locks on to it delivering a 1x Timing-Safe output clock. P3PS850BH has a Frequency Selection control that facilitates selecting one of the two frequency ranges. The device has an SSEXTR pin to select different deviations depending upon the value of an external resistor connected at this pin to GND. P3PS850BH has an MR pin for selecting one of the two Modulation Rates. PD#/OE provides the Power Down option. Outputs will be tri-stated when power down is active.

P3PS850BH operates over a supply voltage range of 2.3 V to 3.6 V, and is available in an 8 pin WDFN (2mmx2mm) package.

Features

- 1x , LVCMOS Timing-Safe Peak EMI Reduction
- Analog Frequency Deviation Selection
- Two different Modulation Rate Selection
- Power Down option for Power Save
- Output buffer strength: 16 mA

Applications

- Consumer electronic applications

End Products

- mobile phones, Camera modules, MFP and DPF.

Part Electrical Specifications

Product	Compliance	Status	V _{DD} Typ (V)	f _{in} Typ (MHz)	f _{out} Typ (MHz)	Deviation Type	Features	T _A Min (°C)	T _A Max (°C)	Package Type
P3PS850BHG-08CR	Pb-free Halide free	Active	2.3-3.6	18-72	18-72	Analog	PDB	-20	85	WDFN-8

For more information please contact your local sales support at www.onsemi.com.

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