Product Overview

NLX1G125: Non-Inverting Buffer, 3-State

For complete documentation, see the data sheet

Product Description

The NLX1G125 is an advanced high-speed 2-input CMOS non-inverting 3-state buffer in ultra-small footprint. The NLX1G125 input structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

Features

- High Speed: tPD = 2.7 ns (Typ) @ VCC = 5.0 V
- Designed for 1.65 V to 5.5 V VCC Operation
- Low Power Dissipation: ICC = 1 A (Max) at TA = 25C
- 24 mA Balanced Output Source and Sink Capability
- Balanced Propagation Delays
- Overvoltage Tolerant (OVT) Input Pins
- UltraSmall Packages
- These are PbFree Devices

Applications

- I/O Interface

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
NLX1G125FCT1G	Pb-free Halide free	Active	1	3-State	1.65	5.5	2.7	32	Flip-Chip-5

For more information please contact your local sales support at www.onsemi.com

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