



## Product Overview

### NLU3G17: Triple Non-Inverting Buffer with Schmitt Trigger Output

For complete documentation, see the [data sheet](#)

#### Product Description

The NLU3G17 MiniGate™ is an advanced high speed CMOS triple non inverting Schmitt trigger buffer in ultra small footprint. The NLU3G17 input and output structures provide protection when voltages up to 7.0 volts are applied, regardless of the supply voltage. The NLU3G17 can be used to enhance noise immunity or to square up slowly changing waveforms.

#### Features

- High Speed:  $t_{PD} = 4.0$  ns (Typ) at  $V_{CC} = 5.0$  V
- Low Power Dissipation:  $I_{CC} = 1$   $\mu$ A (Max) at  $T_A = 25^\circ\text{C}$
- Power Down Protection Provided on inputs
- Balanced Propagation Delays
- Overvoltage Tolerant (OVT) Input and Output Pins
- Ultra Small Pb Free Package

#### Applications

- General Purpose Logic

#### Part Electrical Specifications

Product	Compliance	Status	Channels	Output	$V_{CC}$ Min (V)	$V_{CC}$ Max (V)	$t_{pd}$ Max (ns)	$I_O$ Max (mA)	Package Type
NLU3G17MUTAG	Pb-free Halide free	Active	3	CMOS	1.65	5.5	8.6	8	UDFN-8

For more information please contact your local sales support at [www.onsemi.com](http://www.onsemi.com)

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