

Product Overview

NLAS5223C: Ultra-Low 0.35 Ω Dual SPDT Analog Switch

For complete documentation, see the data sheet.

The NLAS5223C is an advanced CMOS analog switch fabricated in Sub-micron silicon gate CMOS technology. The device is a dual Independent Single Pole Double Throw (SPDT) switch featuring Ultra-Low RON of 0.35 Ω , at VCC = 4.3 V. The part also features guaranteed Break Before Make (BBM) switching, assuring the switches never short the driver.

Features

- Ultra-Low RON, 0.35 (typ) at VCC = 4.3 V
- Single Supply Operation from 1.65-4.5 V
- High Off-Channel Isolation
- Low Standby Current, 50 nA
- RON Flatness of 0.15
- 1.4 x 1.8 x 0.55 mm UQFN10 Pb-Free

Applications

- Cell Phone Audio Block
- Speaker and Earphone Switching
- Ring-Tone Chip/Amplifier Switching
- Modems

Benefits

- Higher Quality Sound
- Works in most platforms without having to create a new power rail
- Reduces distortion due to crosstalk
- Supports longer battery life
- Low distortion
- Supports small form factor products

End Products

- Smartphones

Part Electrical Specifications

Product	Compliance	Status	Channels	Number of Switches	Configuration	I _{cc} Max (μ A)	r _{on} Max (Ω)	V _{CC} Min (V)	V _{CC} Max (V)	Package Type
NLAS5223CLMUTAG	Pb-free	Active	2	4	SPDT	2	0.35	1.65	4.5	UQFN-10
	Halide free									
NLAS5223CMUTAG	Pb-free	Active	2	4	SPDT	2	0.35	1.65	4.5	UQFN-10
	Halide free									

For more information please contact your local sales support at www.onsemi.com.

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