



Product Overview

NB3N2304NZ: Clock / Data Fanout Buffer, 1:4, 3.3 V

For complete documentation, see the data sheet

Product Description

The NB3N2304NZ is a low skew 1 to 4 clock fanout buffer, designed for high speed clock distribution such as in PCI-X applications. The NB3N2304NZ guarantees low output-to-output skew. Optimal design, layout and processing minimizes skew within a device and from device-to-device. The Output Enable (OE) pin forces the outputs LOW when LOW.

Features

- Input/Output Clock Frequency up to 140 MHz
- Low Skew Outputs (100 ps)
- Operating Range: VDD = 3.0 V to 3.6 V
- Packaged in 8pin TSSOP, 4.4 mm x 3 mm
- Industrial Temperature Range

Applications

- Ideal for PCIX and networking clocks
- CMOS Low Skew Clock Distribution

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V _{CC} Typ (V)	t _{Jitter} RMS Typ (ps)	t _{skew(o-q)} Max (ps)	t _{pd} Typ (ns)	t _R & t _F Max (ps)	f _{max} Clock Typ (MHz)	f _{max} Data Typ (Mbps)	Package Type
NB3N2304NZDTG	Pb-free Halide free	Active	Buffer	1	1:4	CMOS	CMOS	3.3		100	3.5	1500	140		TSSOP-8
NB3N2304NZDTR2G	Pb-free Halide free	Active	Buffer	1	1:4	CMOS	CMOS	3.3		100	3.5	1500	140		TSSOP-8
NB3N2304NZMNR4G	Pb-free Halide free	Active	Buffer	1	1:4	CMOS	CMOS	3.3		100	3.5	1500	140		DFN-8

For more information please contact your local sales support at www.onsemi.com

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