



Product Overview

NB3N111K: Clock / Data Fanout Buffer, 1:10 Differential, 3.3 V, with HCSL Outputs

For complete documentation, see the data sheet

Product Description

The NB3N111K is a differential 1 to 10 Clock and Data fanout buffer with Highspeed Current Steering Logic (HCSL) outputs optimized for ultra low propagation delay variation. The NB3N111K is designed with HCSL clock distribution and FBDIMM applications in mind.

Features	Benefits
<ul style="list-style-type: none"> Typical Input Clock Frequency 100, 133, 166, or 400 MHz 0.1 ps Typical RMS Phase Jitter Operating Range: VCC = 3.0 V to 3.6 V with GND = 0 V 220 ps Typical Rise and Fall Times 800 ps Typical Propagation Delay Delta tpd 100 ps Maximum Propagation Delay Variation per Diff Pair Differential HCSL Output Levels 	<ul style="list-style-type: none"> Versatile Design Capabilities Best in class for jitter performance Conformance with Industry Standards

Applications	End Products
<ul style="list-style-type: none"> Clock Distribution PCIe I, II, III Networking High End Computing 	<ul style="list-style-type: none"> Servers Routers FBDIMM Memory Card

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V _{CC} Typ (V)	t _{jitter} RMS Typ (ps)	t _{skew} (o-o) Max (ps)	t _{pd} Typ (ns)	t _R & t _F Max (ps)	f _{max} Clock Typ (MHz)	f _{max} Data Typ (Mbps)	Package Type
NB3N111KMNG	Pb-free Halide free	Active	Buffer	1	1:10	CMOS ECL HCSL LVDS TTL	HCSL	3.3	0.1	100	0.8	400	400	400	QFN-32
NB3N111KMNR4G	Pb-free Halide free	Active	Buffer	1	1:10	CMOS ECL HCSL LVDS TTL	HCSL	3.3	0.1	100	0.8	400	400	400	QFN-32

For more information please contact your local sales support at www.onsemi.com

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