



Product Overview

NB3M8302C: LVCMOS/LVTTL Low Skew Fanout Buffer; 1:2, 3.3 V, 200 MHz

For complete documentation, see the data sheet

Product Description

The NB3M8302C is 1:2 fanout buffer with LVCMOS/LVTTL input and output. The device supports the core supply voltage of 3.3 V (VDD pin) and output supply voltage of 2.5 V or 3.3 V (VDDO pin). The VDDO pin powers the two single ended LVCMOS/LVTTL outputs.

The NB3M8302C is Form, Fit and Function (pin to pin) compatible to ICS8302 (ICS8302AM). The NB3M8302C is available for commercial and industrial operating temperature range.

Features

- Input Clock Frequency up to 200 MHz
- Low Output to Output Skew: 25 ps typical
- Low Part to Part Skew: 250 ps typical
- Low Additive RMS Phase Jitter
- Input Clock Accepts LVCMOS/ LVTTL Levels

Applications

- Clock Distribution
- Networking and Data Communications
- High-End Computing

End Products

- Ethernet Switches / Routers
- Servers
- Test and Measurement
- ATE

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V _{CC} Typ (V)	t _{jitter} RMS Typ (ps)	t _{skew(o-to-o)} Max (ps)	t _{pd} Typ (ns)	t _R & t _F Max (ps)	f _{max} Clock Typ (MHz)	f _{max} Data Typ (Mbps)	Package Type
NB3M8302CDG	Pb-free Halide free	Active	Buffer	1	1:2	CMOS TTL	CMOS TTL	2.5 3.3		85	2.65	650 800	200		SOIC-8
NB3M8302CDR2G	Pb-free Halide free	Active	Buffer	1	1:2	CMOS TTL	CMOS TTL	2.5 3.3		85	2.65	650 800	200		SOIC-8

For more information please contact your local sales support at www.onsemi.com

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