

Product Overview

HCPL062N: 10MBit/s High Performance 3.3V Dual Channel Optocoupler

For complete documentation, see the data sheet.

The HCPL062N optocouplers consist of an AlGaAs, LED optically coupled to a very high speed integrated photodetector logic gate consisting of bipolar transistors on a CMOS process for reduced power consumption. The output features an open collector, thereby permitting wired OR outputs. The devices are housed in a compact small-outline package. The coupled parameters are guaranteed over the temperature range of -40°C to +85°C. An internal noise shield and provides superior common mode rejection.

Features

- Compact SO8 package
- Very high speed – 10MBit/s
- Superior CMR – 25kV/μs minimum (1,000 volts common mode)
- Logic gate output
- Wired OR-open collector
- Fixed threshold detector design minimizes thermal impact on switching times
- U.L. recognized (File # E90700)

Applications

- Energy Generation & Distribution
- Industrial Motor
- Uninterruptible Power Supply

Part Electrical Specifications

Product	Compliance	Status	Data Rate	V _{CC} (Max) (V)	V _{CC} (Min) (V)	Channels	I _{FT} (Max) (mA)	CTR (Min) (%)	CTR (Max) (%)	CTR Tested @IF	V _{OL} (Max) (V)	t _{PHL} (Max) (ns)	t _{PLH} (Max) (ns)	PWD (Max) (ns)	CMR (Min) (kV/μs)	V _{ISO} (Min) (V)	T _{OPR} (Min) (°C)	T _{OPR} (Max) (°C)	Package Type
HCPL062N	Pb-free	Active	10	3.3		2	5	-	-	-	0.6	75	90	25	25	3750	-40	85	SOIC-8
HCPL062NR2	Pb-free	Active	10	3.3		2	5	-	-	-	0.6	75	90	25	25	3750	-40	85	SOIC-8

For more information please contact your local sales support at www.onsemi.com.

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