

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

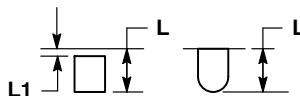
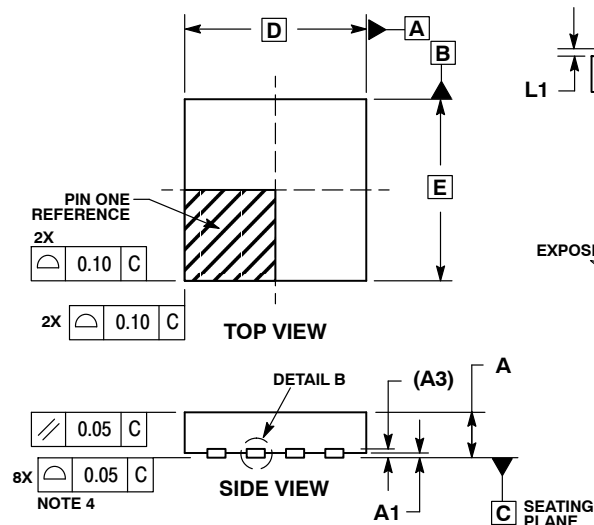
ON Semiconductor®



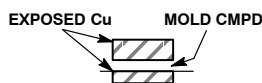
SCALE 2:1

**WDFN8, 3x3, 0.65P**  
CASE 506BC-01  
ISSUE A

DATE 28 MAY 2008



**DETAIL A**  
OPTIONAL  
CONSTRUCTIONS

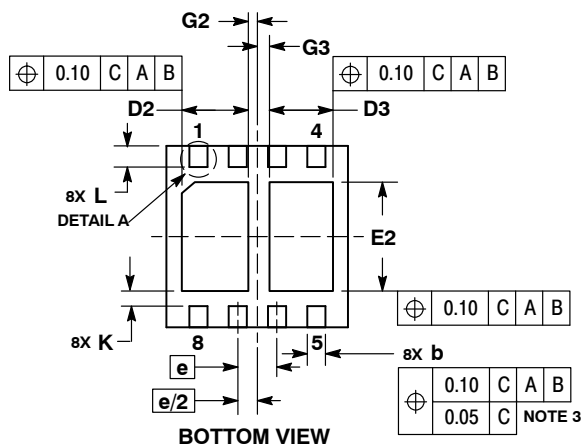


**DETAIL B**  
OPTIONAL  
CONSTRUCTIONS

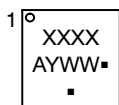
**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30mm.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

MILLIMETERS		
DIM	MIN	MAX
A	0.70	0.80
A1	0.00	0.05
A3	0.20	REF
b	0.25	0.35
D	3.00	BSC
D2	1.00	1.20
D3	0.95	1.15
E	3.00	BSC
E2	1.70	1.90
e	0.65	BSC
G2	0.15	BSC
G3	0.20	BSC
K	0.20	---
L	0.25	0.45
L1	---	0.15



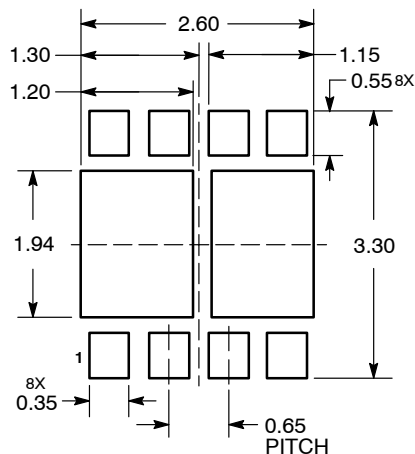
**GENERIC MARKING DIAGRAM\***



- XXXX = Specific Device Code
- A = Assembly Location
- Y = Year
- WW = Work Week
- = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "▪", may or may not be present.

**SOLDERING FOOTPRINT\***



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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<b>DESCRIPTION:</b>	<b>WDFN8, 3X3, 0.65P, DUAL FLAG</b>	<b>PAGE 1 OF 1</b>

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