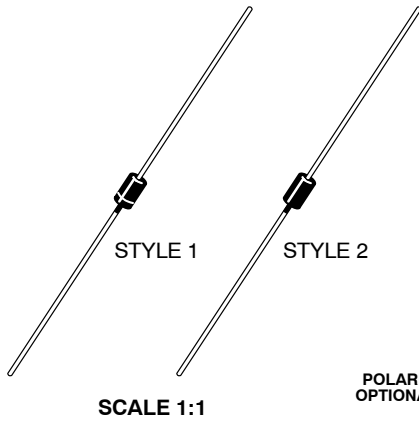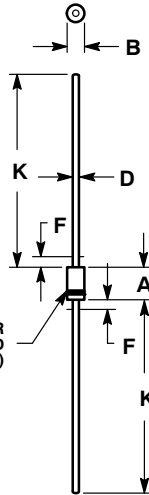**ON Semiconductor®**

AXIAL LEAD
CASE 59AB
ISSUE O

DATE 07 DEC 2011

SCALE 1:1

STYLE 1   STYLE 2

POLARITY INDICATOR
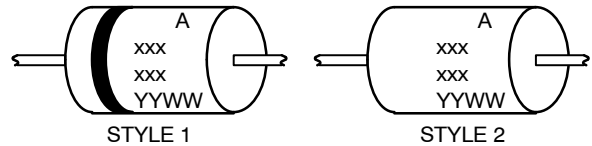OPTIONAL AS NEEDED
(SEE STYLES)

NOTES:
1. CONTROLLING DIMENSION: INCHES.
2. PACKAGE CONTOUR IS OPTIONAL WITHIN DIMENSIONS A AND B. HEAT SLUGS, IF ANY, SHALL BE WITHIN DIMENSION B BUT NOT SUBJECT TO ITS MINIMUM VALUE.
3. DIMENSION A DEFINES THE ENTIRE BODY INCLUDING HEAT SLUGS.
4. DIMENSION B IS MEASURED AT THE MAXIMUM DIAMETER OF THE BODY.
5. POLARITY SHALL BE DENOTED BY A CATHODE BAND.
6. LEAD DIAMETER, D, IS NOT CONTROLLED IN ZONE F.
7. ALL RULES AND NOTES ASSOCIATED WITH JEDEC DO–41 OUTLINE SHALL APPLY

DIM	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX
A	0.161	0.205	4.10	5.20
B	0.079	0.106	2.00	2.70
D	0.028	0.034	0.71	0.86
F	---	0.050	---	1.27
K	0.540	---	13.70	---

STYLE 1:
PIN 1. CATHODE (POLARITY BAND)
    2. ANODE

STYLE 2:
NO POLARITY

GENERIC
MARKING DIAGRAM*

STYLE 1   STYLE 2

xxxxxxx = Specific Device Code
A   = Assembly Location
YY   = Year
WW   = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot " ▪", may or may not be present.

DOCUMENT NUMBER:	98AON66049E	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	AXIAL LEAD		PAGE 1 OF 1