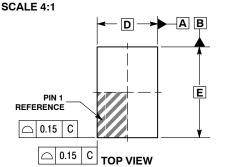
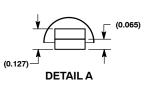
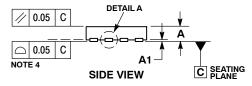


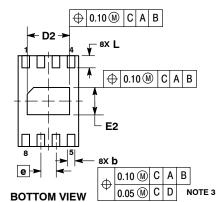
UDFN8 3.0x2.0, 0.5P CASE 517BU-01 ISSUE O

**DATE 06 APR 2011** 

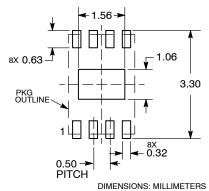








## **RECOMMENDED MOUNTING FOOTPRINT**



## NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. DIMENSIONS 6 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN
- 0.15 AND 0.25 MM FROM TERMINAL TIP. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS		
DIM	MIN	MAX	
Α	0.45	0.55	
A1	0.00	0.05	
b	0.20	0.30	
D	2.00 BSC		
D2	1.35	1.45	
E	3.00 BSC		
E2	0.85	0.95	
е	0.50 BSC		
L	0.35	0.45	

## **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

= Assembly Location Code

= Assembly Lot LL

Υ = Year

= Month

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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