

MECHANICAL CASE OUTLINE

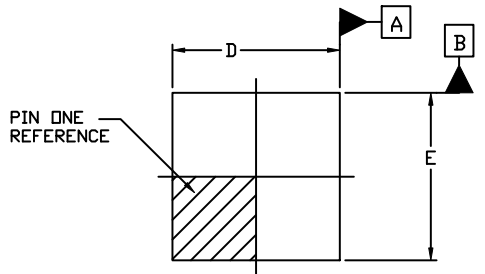
PACKAGE DIMENSIONS

ON Semiconductor®



DFNW10 3x3, 0.5P CASE 507AM ISSUE A

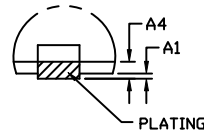
DATE 12 JUN 2018



TOP VIEW

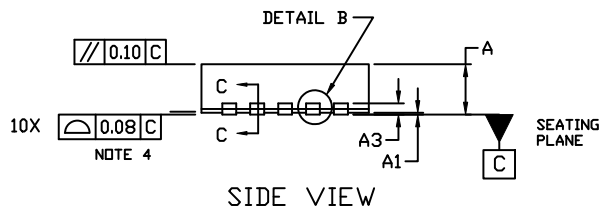
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION *b* APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

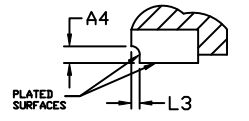


DETAIL B

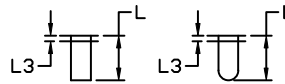
DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.80	0.90	1.00
A1	0.00	---	0.05
A3	0.20 REF		
A4	0.10	---	---
<i>b</i>	0.20	0.25	0.30
D	2.85	3.00	3.15
D2	2.40	2.50	2.60
E	2.85	3.00	3.15
E2	1.70	1.80	1.90
<i>e</i>	0.50 BSC		
<i>k</i>	0.19 REF		
L	0.35	0.40	0.45
L3	---	0.05	0.10



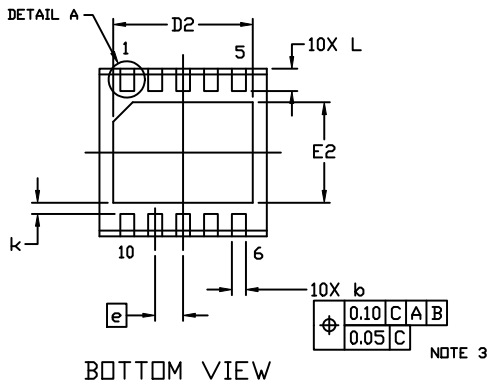
SIDE VIEW



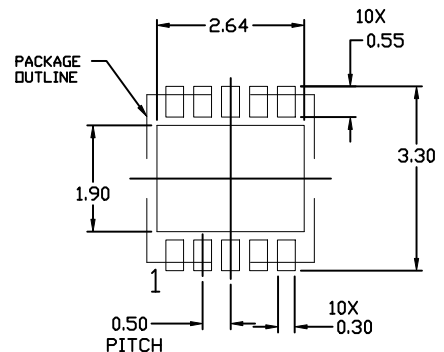
SECTION C-C



ALTERNATE CONSTRUCTION
DETAIL A

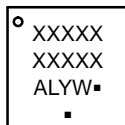


BOTTOM VIEW



RECOMMENDED MOUNTING FOOTPRINT

GENERIC MARKING DIAGRAM*



- XXXXX = Specific Device Code
- A = Assembly Location
- L = Wafer Lot
- Y = Year
- W = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	DFNW10 3x3, 0.5P	PAGE 1 OF 1

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