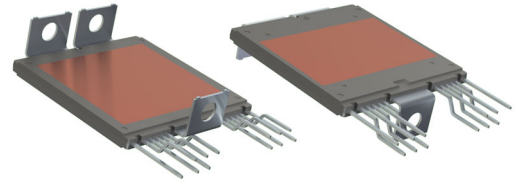


Automotive 750 V, 800 A Dual Side Cooling Half-Bridge Power Module

VE-Trac™ Dual Gen II NVG800A75L4DSC2



AHPM15-CEA
 CASE MODHS

Product Description

The NVG800A75L4DSC2 is part of a family of power modules with dual side cooling and compact footprints for Hybrid (HEV) and Electric Vehicle (EV) traction inverter application.

The module consists of two narrow mesa Field Stop (FS4) IGBTs in a half-bridge configuration. The chipset utilizes the new narrow mesa IGBT technology in providing high current density and robust short circuit protection with higher blocking voltage to deliver outstanding performance in EV traction applications.

Liquid cooling heatsink reference design, loss models and CAD models are available to support customers in inverter designs.

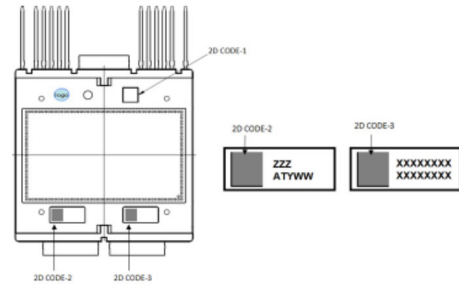
Features

- Dual-Side Cooling
- Integrated Chip Level Temperature and Current Sensor
- $T_{vj\ max} = 175^{\circ}\text{C}$ for Continuous Operation
- Low-stray Inductance
- Low Conduction and Switching Losses
- Automotive Grade
- 4.2 kV Isolated DBC Substrate
- AEC Qualified and PPAP Capable
- This Device is Pb-Free and is RoHS Compliant

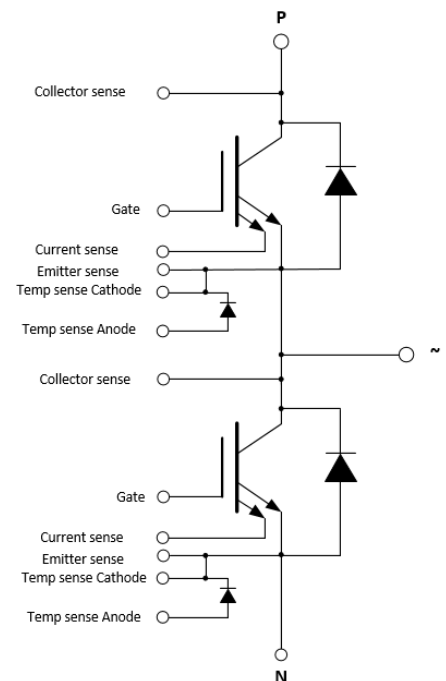
Typical Applications

- Hybrid and Electric Vehicle Traction Inverter
- High Power DC-DC Converter

MARKING DIAGRAM



ZZZ = Assembly Lot Code
 AT = Assembly & Test Location
 Y = Year
 WW = Work Week
 XXXX = Specific Device Code



ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

VE-Trac™ Dual Gen II NVG800A75L4DSC2

PIN DESCRIPTION

Pin #	Pin	Pin Function Description	Pin Arrangement
1	N	Low Side Emitter	
2	P	High Side Collector	
3	H/S COLLECTOR SENSE	High Side Collector Sense	
4	H/S CURRENT SENSE	High Side Current Sense	
5	H/S EMITTER SENSE	High Side Emitter Sense	
6	H/S GATE	High Side Gate	
7	H/S TEMP SENSE (CATHODE)	High Side Temp sense Diode Cathode	
8	H/S TEMP SENSE (ANODE)	High Side Temp sense Diode Anode	
9	~	Phase Output	
10	L/S CURRENT SENSE	Low Side Current Sense	
11	L/S EMITTER SENSE	Low Side Emitter Sense	
12	L/S GATE	Low Side Gate	
13	L/S TEMP SENSE (CATHODE)	Low Side Temp sense Diode Cathode	
14	L/S TEMP SENSE (ANODE)	Low Side Temp sense Diode Anode	
15	L/S COLLECTOR SENSE	Low Side Collector Sense	

Materials

DBC Substrate: Al₂O₃ isolated substrate, basic isolation, and copper on both sides.

Lead Frame

Copper with Tin electro-plating.

Flammability Information

All materials present in the power module meet UL flammability rating class 94V-0.

MODULE CHARACTERISTICS

Symbol	Parameter	Rating	Unit		
T _{vj}	Continuous Operating Junction Temperature Range	-40 to 175	°C		
T _{STG}	Storage Temperature range	-40 to 125	°C		
V _{ISO}	Isolation Voltage, AC, f = 50 Hz, t = 1 s	4200	V		
Creepage	Minimum: Terminal to Terminal	5.0	mm		
Clearance	Minimum: (Note 1) Terminal to Terminal	3.2	mm		
CTI	Comparative Tracking Index	>600			
		Min	Typ	Max	
L _{sCE}	Stray Inductance		8		nH
R _{CC'+EE'}	Module Lead Resistance, Terminals - Chip		0.15		mΩ
G	Module Weight		75		g
M	M4 Screws for Module Terminals			2.2	Nm

1. Verified by design / not by test.

VE-Trac™ Dual Gen II NVG800A75L4DSC2

ABSOLUTE MAXIMUM RATINGS (T_{VJ} = 25°C, Unless Otherwise Specified)

Symbol	Parameter	Rating	Unit
IGBT			
V _{CES}	Collector to Emitter Voltage	750	V
V _{GES}	Gate to Emitter Voltage	±20	V
I _{CN}	Implemented Collector Current	800	A
I _{C nom}	Continuous DC Collector Current, T _{VJmax} = 175°C, T _F = 65°C, ref. heatsink	550 ⁽¹⁾	A
I _{CRM}	Pulsed Collector Current @ V _{GE} = 15 V, t _p = 1 ms	1600	A

DIODE

V _{RRM}	Repetitive peak reverse voltage	750	V
I _{FN}	Implemented Forward Current	800	A
I _F	Continuous Forward Current, T _{VJmax} = 175°C, T _F = 65°C, ref. heatsink	420 ⁽¹⁾	A
I _{FRM}	Repetitive Peak Forward Current, t _p = 1 ms	1600	A
I ² t value	Surge current capability, V _R = 0 V, t _p = 10 ms, T _{VJ} = 150°C T _{VJ} = 175°C	20000 18000	A ² s

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2. Verified by characterization, not by test.

THERMAL CHARACTERISTICS (Verified by characterization, not by test.)

Symbol	Parameter	Min	Typ	Max	Unit
IGBT.R _{th,J-C}	Effective Rth, Junction to Case ⁽³⁾		0.05	0.07	°C/W
IGBT.R _{th,J-F}	Effective Rth, Junction to Fluid, λ _{TIM} = 6 W/m-K, F = 660 N 10 L/min, 65°C, 50/50 EGW, Ref. Heatsink		0.128		°C/W
Diode.R _{th,J-C}	Effective Rth, Junction to Case ⁽³⁾		0.07	0.09	°C/W
Diode.R _{th,J-F}	Effective Rth, Junction to Fluid, λ _{TIM} = 6 W/m-K, F = 660 N 10 L/min, 65°C, 50/50 EGW, Ref. Heatsink		0.186		°C/W

3. For the measurement point of case temperature (T_c), DBC discoloration, picker circle print is allowed, please refer to the VE-Trac Dual assembly guide for additional details about acceptable DBC surface finish.

VE-Trac™ Dual Gen II NVG800A75L4DSC2

CHARACTERISTICS OF IGBT (T_{vj} = 25°C, Unless Otherwise Specified)

Parameters		Conditions	Min	Typ	Max	Unit
V _{CESAT}	Collector to Emitter Saturation Voltage (Terminal)	V _{GE} = 15 V, I _C = 600 A, T _{vj} = 25°C T _{vj} = 150°C T _{vj} = 175°C V _{GE} = 15 V, I _C = 800 A, T _{vj} = 25°C T _{vj} = 150°C T _{vj} = 175°C	–	1.30 1.42 1.44 1.43 1.63 1.66	1.69	V
I _{CES}	Collector to Emitter Leakage Current	V _{GE} = 0, V _{CE} = 750 V T _{vj} = 25°C T _{vj} = 175°C	– –	– 8	1 –	mA mA
I _{GES}	Gate – Emitter Leakage Current	V _{CE} = 0, V _{GE} = ± 20 V	–	–	±400	nA
V _{th}	Threshold Voltage	V _{CE} = V _{GE} , I _C = 500 mA	4.5	5.5	6.5	V
Q _G	Total Gate Charge	V _{GE} = –8 to 15 V, V _{CE} = 400 V	–	1.7	–	μC
R _{Gint}	Internal gate resistance		–	2	–	Ω
C _{ies}	Input Capacitance	V _{CE} = 30 V, V _{GE} = 0 V, f = 100 KHz	–	43	–	nF
C _{oes}	Output Capacitance	V _{CE} = 30 V, V _{GE} = 0 V, f = 100 KHz	–	1.48	–	nF
C _{res}	Reverse Transfer Capacitance	V _{CE} = 30 V, V _{GE} = 0 V, f = 100 KHz	–	0.19	–	nF
T _{d,on}	Turn on delay, inductive load	I _C = 600 A, V _{CE} = 400 V T _{vj} = 25°C V _{GE} = +15/–8 V T _{vj} = 150°C R _{g,on} = 4.7 Ω T _{vj} = 175°C	–	377 382 382	–	ns
T _r	Rise time, inductive load	I _C = 600 A, V _{CE} = 400 V T _{vj} = 25°C V _{GE} = +15/–8 V T _{vj} = 150°C R _{g,on} = 4.7 Ω T _{vj} = 175°C	–	104 127 132	–	ns
T _{d,off}	Turn off delay, inductive load	I _C = 600 A, V _{CE} = 400 V T _{vj} = 25°C V _{GE} = +15/–8 V T _{vj} = 150°C R _{g,off} = 15 Ω T _{vj} = 175°C	–	917 1042 1075	–	ns
T _f	Fall time, inductive load	I _C = 600 A, V _{CE} = 400 V T _{vj} = 25°C V _{GE} = +15/–8 V T _{vj} = 150°C R _{g,off} = 15 Ω T _{vj} = 175°C	–	129 199 212	–	ns
E _{ON}	Turn-On Switching Loss (including diode reverse recovery loss)	I _C = 600 A, V _{CE} = 400 V, V _{GE} = +15/–8 V, L _s = 20 nH, R _{g,on} = 4,7 Ω di/dt (T _{vj} = 25°C) = 4.77 A/ns di/dt (T _{vj} = 175°C) = 3.78 A/ns T _{vj} = 25°C T _{vj} = 150°C T _{vj} = 175°C	–	 22.93 35.87 37.70	–	mJ
E _{OFF}	Turn-Off Switching Loss	I _C = 600 A, V _{CE} = 400 V, V _{GE} = +15/–8 V, L _s = 20 nH, R _{g,off} = 15 Ω dv/dt (T _{vj} = 25°C) = 2.79 V/ns dv/dt (T _{vj} = 175°C) = 2.05 V/ns T _{vj} = 25°C T _{vj} = 150°C T _{vj} = 175°C	–	 33.57 47.30 49.09	–	mJ
E _{SC}	Minimum Short Circuit Energy Withstand	V _{GE} = 15 V, V _{CC} = 400 V T _{vj} = 25°C T _{vj} = 175°C	5	5		J

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

VE-Trac™ Dual Gen II NVG800A75L4DSC2

CHARACTERISTICS OF INVERSE DIODE ($T_{VJ} = 25^{\circ}\text{C}$, Unless Otherwise Specified)

Parameters		Conditions	Min	Typ	Max	Unit
V_F	Diode Forward Voltage (Terminal)	$V_{GE} = 0\text{ V}$, $I_C = 600\text{ A}$, $T_{VJ} = 25^{\circ}\text{C}$ $T_{VJ} = 150^{\circ}\text{C}$ $T_{VJ} = 175^{\circ}\text{C}$ $V_{GE} = 0\text{ V}$, $I_C = 800\text{ A}$, $T_{VJ} = 25^{\circ}\text{C}$ $T_{VJ} = 150^{\circ}\text{C}$ $T_{VJ} = 175^{\circ}\text{C}$	-	1.39 1.36 1.34 1.49 1.48 1.47	1.80	V
E_{rr}	Reverse Recovery Energy	$I_F = 600\text{ A}$, $V_R = 400\text{ V}$, $V_{GE} = -8\text{ V}$, $R_{g.on} = 4.7\ \Omega$, $-di/dt = 3.12\text{ A/ns}$ (175°C) $T_{VJ} = 25^{\circ}\text{C}$ $T_{VJ} = 150^{\circ}\text{C}$ $T_{VJ} = 175^{\circ}\text{C}$	-	6.05 14.89 17.12	-	mJ
Q_{RR}	Recovered Charge	$I_F = 600\text{ A}$, $V_R = 400\text{ V}$, $V_{GE} = -8\text{ V}$, $R_{g.on} = 4.7\ \Omega$, $-di/dt = 3.12\text{ A/ns}$ (175°C) $T_{VJ} = 25^{\circ}\text{C}$ $T_{VJ} = 150^{\circ}\text{C}$ $T_{VJ} = 175^{\circ}\text{C}$	-	17.12 44.69 52.25	-	μC
I_{rr}	Peak Reverse Recovery Current	$I_F = 600\text{ A}$, $V_R = 400\text{ V}$, $V_{GE} = -8\text{ V}$, $R_{g.on} = 4.7\ \Omega$, $-di/dt = 3.12\text{ A/ns}$ (175°C) $T_{VJ} = 25^{\circ}\text{C}$ $T_{VJ} = 150^{\circ}\text{C}$ $T_{VJ} = 175^{\circ}\text{C}$	-	222 311 325	-	A

SENSOR CHARACTERISTICS ($T_{VJ} = 25^{\circ}\text{C}$, Unless Otherwise Specified)

Parameters		Conditions	Min	Typ	Max	Unit
T_{sense}	Temperature sense	$I_F = 1\text{ mA}$, $T_{VJ} = 25^{\circ}\text{C}$ $T_{VJ} = 150^{\circ}\text{C}$ $T_{VJ} = 175^{\circ}\text{C}$		2.5 1.7 1.5		V
I_{sense}	Current sense	$R_{shunt} = 10\ \Omega$ $I_C = 1600\text{ A}$ $I_C = 800\text{ A}$ $I_C = 100\text{ A}$		505 269 50		mV

4. Measured at chip level

ORDERING INFORMATION

Part Number	Package	Shipping
NVG800A75L4DSC2	AHPM15-CEA Module Case MODHS (Pb-Free)	18 Units / 3x Tube

VE-Trac™ Dual Gen II NVG800A75L4DSC2

TYPICAL CHARACTERISTICS

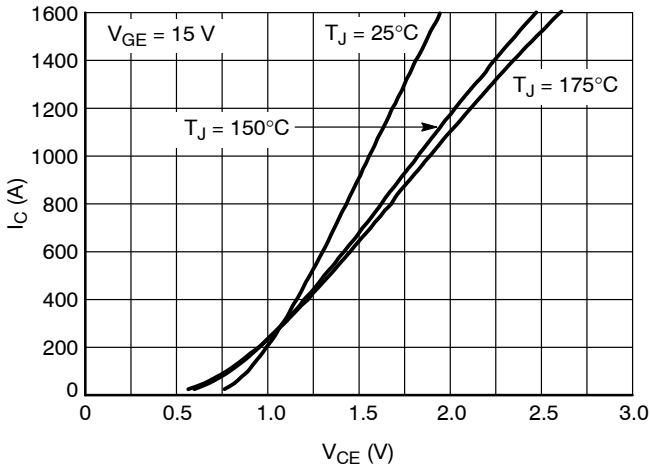


Figure 1. IGBT Output Characteristic

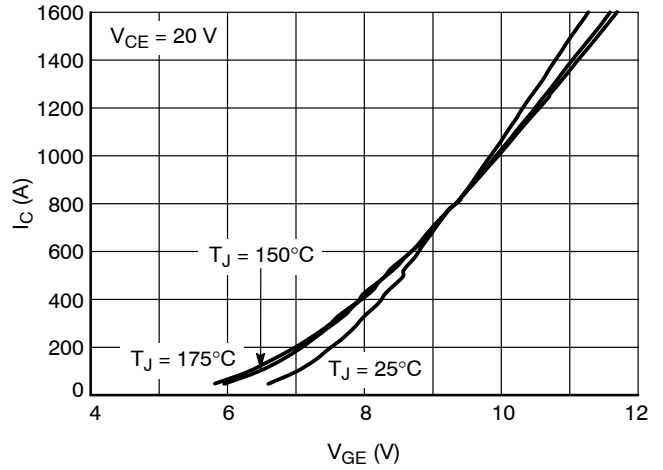


Figure 2. IGBT Transfer Characteristic

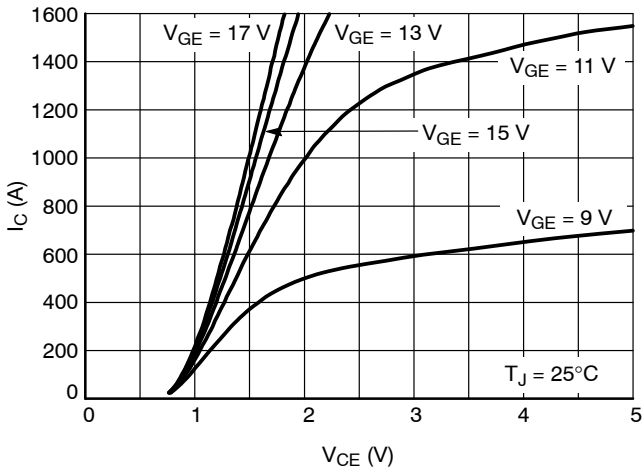


Figure 3. IGBT Output Characteristic, 25°C

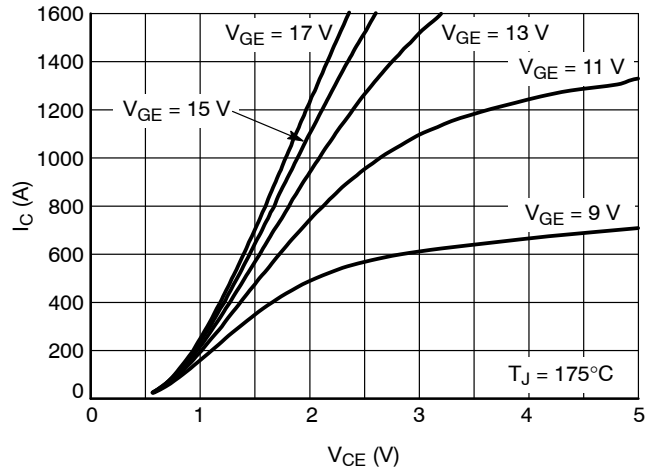


Figure 4. IGBT Output Characteristic, 175°C

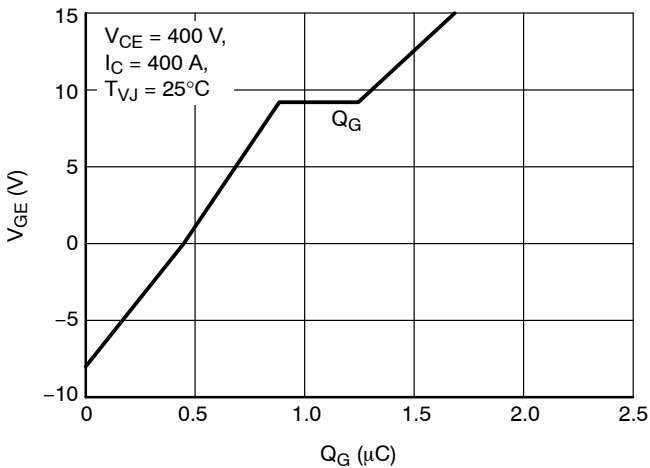


Figure 5. Gate Charge Characteristics

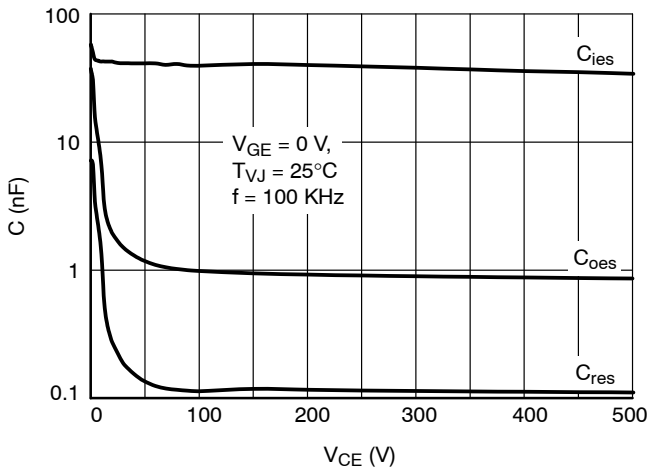


Figure 6. Capacitance Characteristics

VE-Trac™ Dual Gen II NVG800A75L4DSC2

TYPICAL CHARACTERISTICS

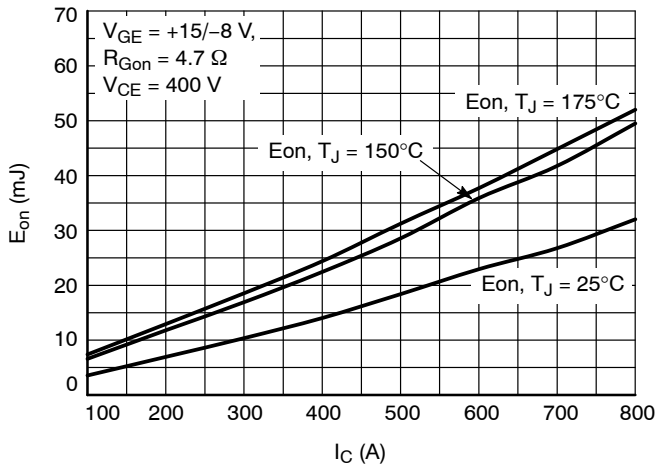


Figure 7. E_{on} vs. I_C

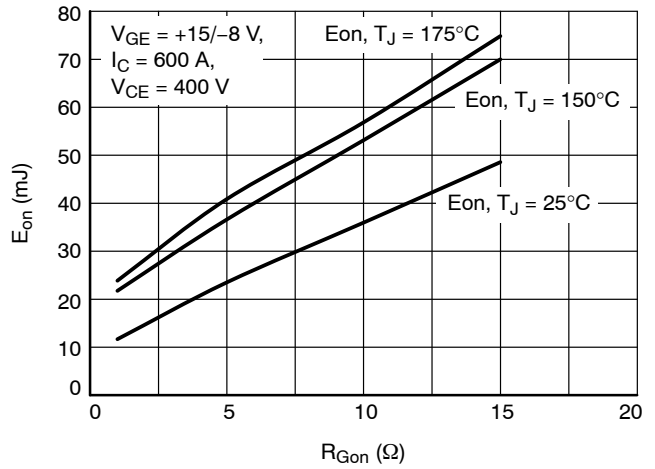


Figure 8. E_{on} vs. R_{Gon}

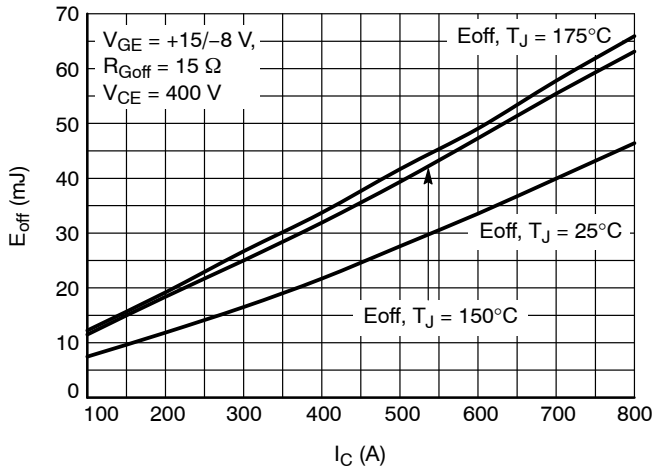


Figure 9. E_{off} vs. I_C

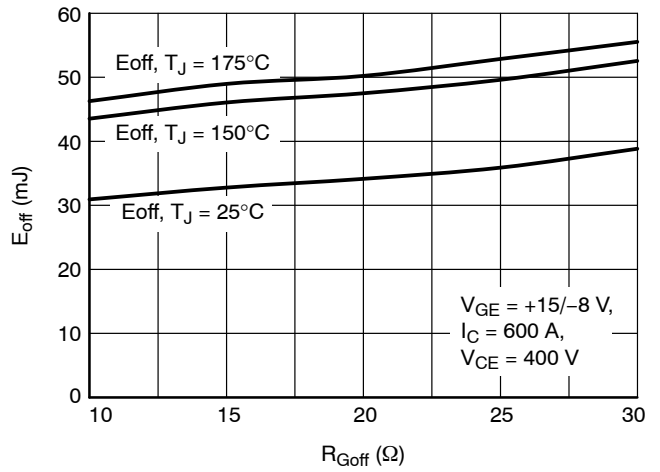


Figure 10. E_{off} vs. R_{Goff}

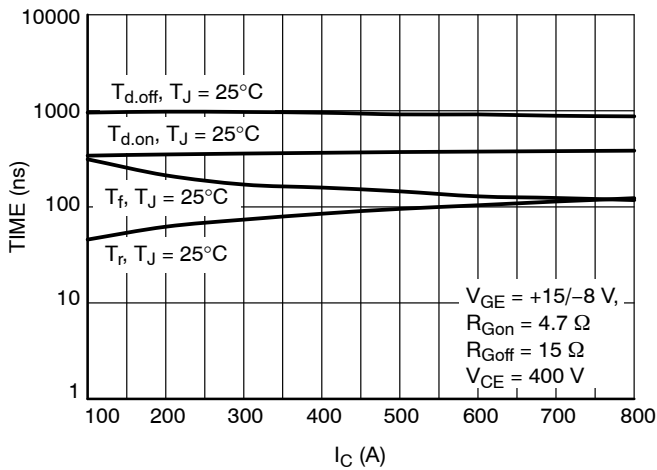


Figure 11. IGBT Switching Times vs. I_C , $T_{VJ} = 25^\circ\text{C}$

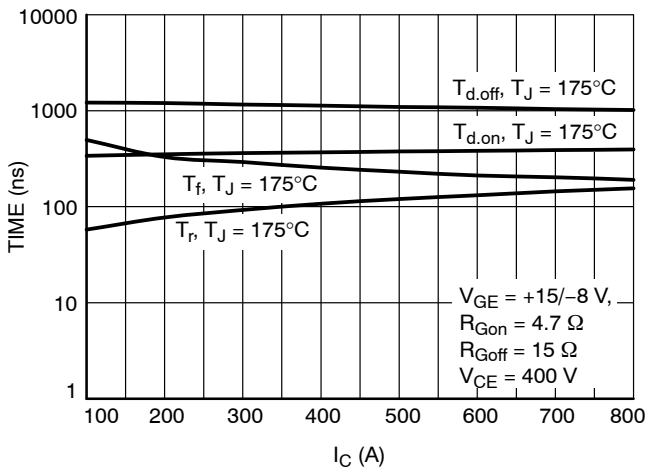


Figure 12. IGBT Switching Times vs. I_C , $T_{VJ} = 175^\circ\text{C}$

VE-Trac™ Dual Gen II NVG800A75L4DSC2

TYPICAL CHARACTERISTICS

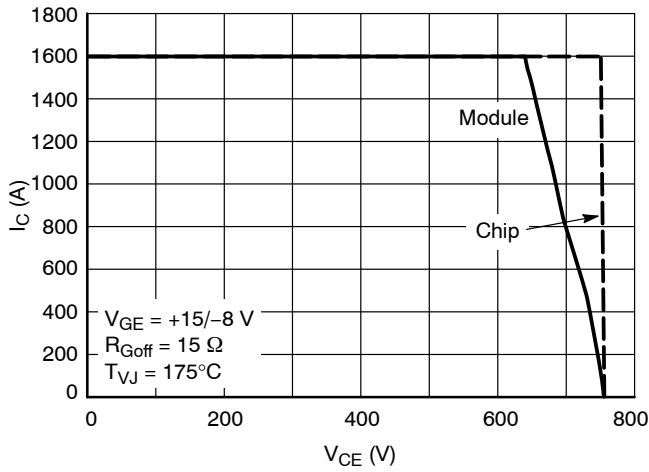


Figure 13. Reverse Bias Safe Operating Area

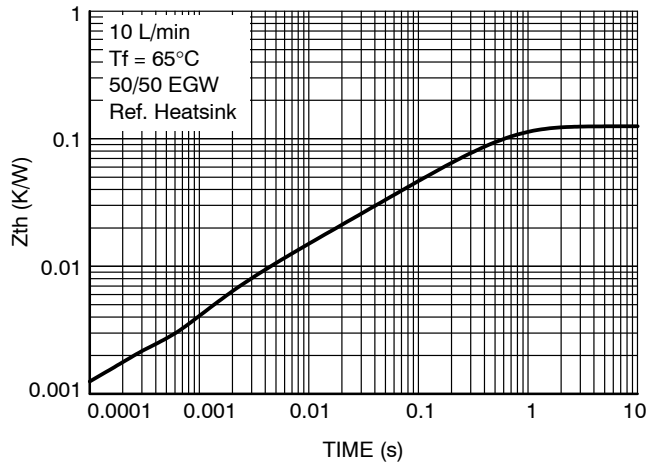


Figure 14. IGBT Transient Thermal Impedance

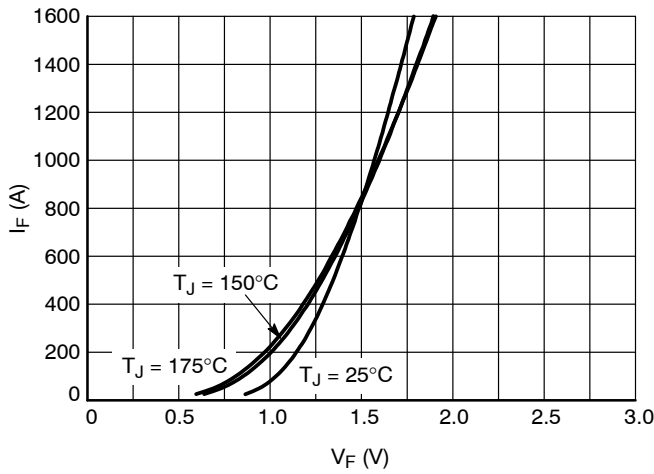


Figure 15. Diode Forward Characteristic

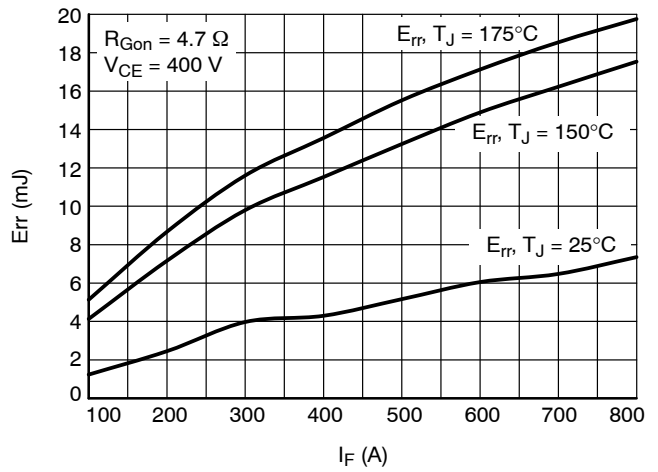


Figure 16. Diode Switching Losses vs. I_F

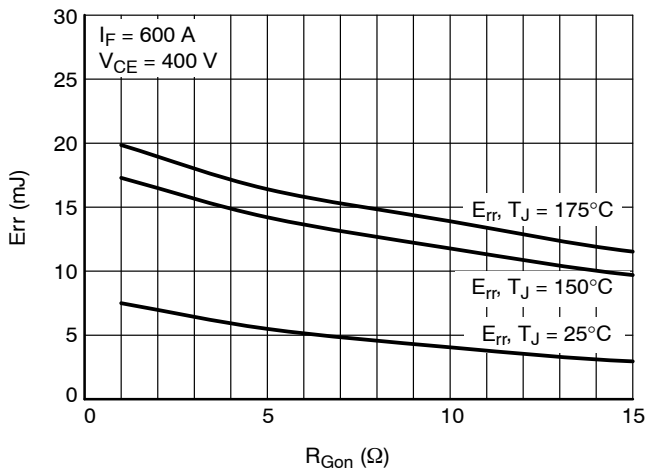


Figure 17. Diode Switching Losses vs. R_{Gon}

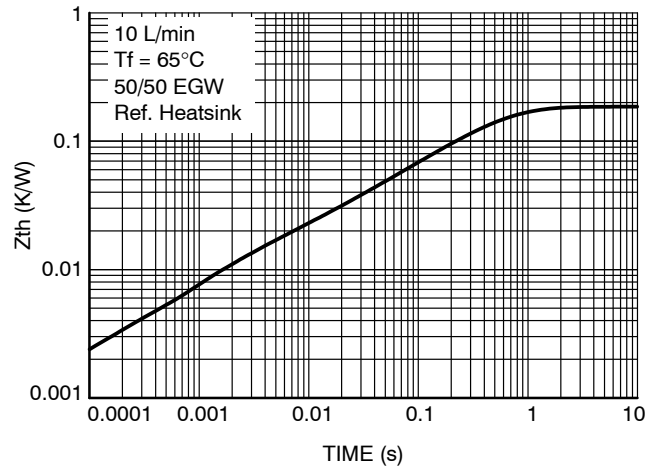


Figure 18. Diode Transient Thermal Impedance

VE-Trac™ Dual Gen II NVG800A75L4DSC2

TYPICAL CHARACTERISTICS

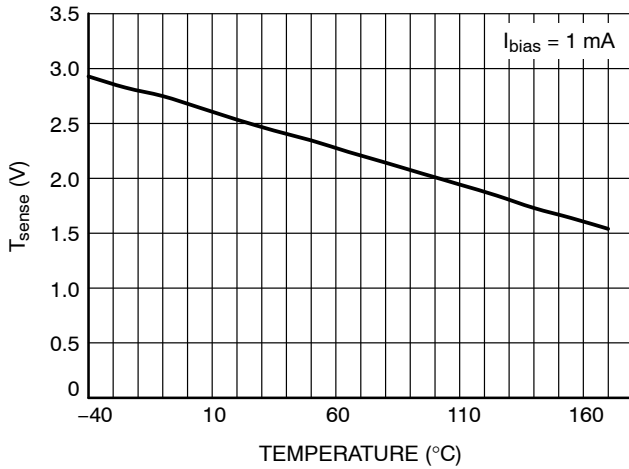


Figure 19. Temperature Sensor Characteristics

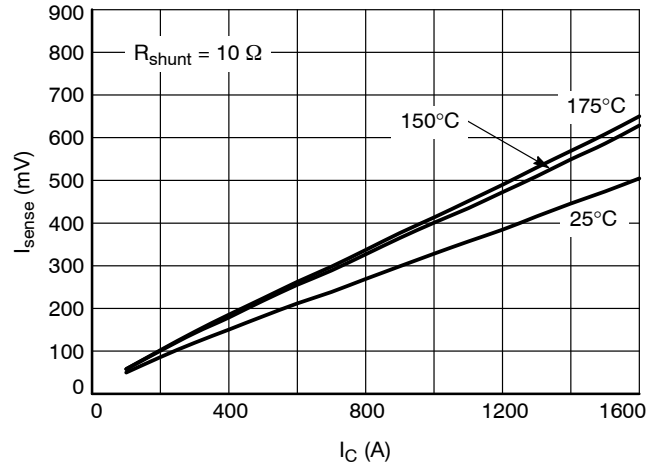


Figure 20. Current Sensor Characteristic

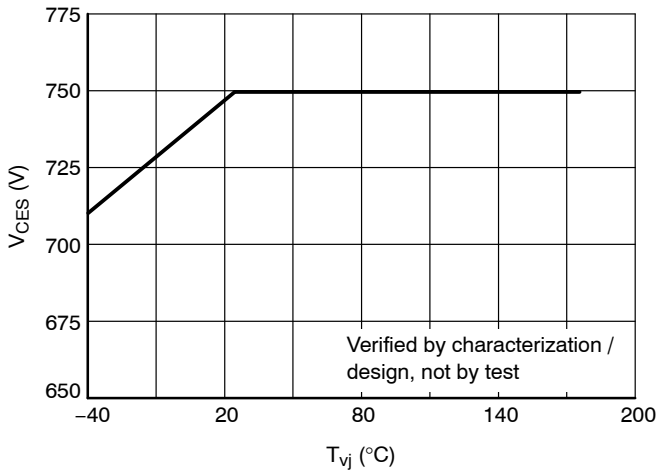
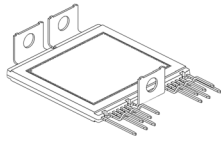


Figure 21. Maximum Allowed V_{CE}

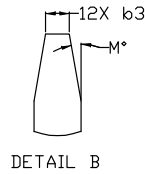
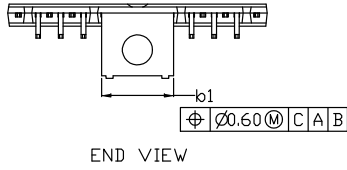
General Note: These are preliminary values measured from a small number of DV units. Values will be updated based on higher quantity of PV measurements.

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



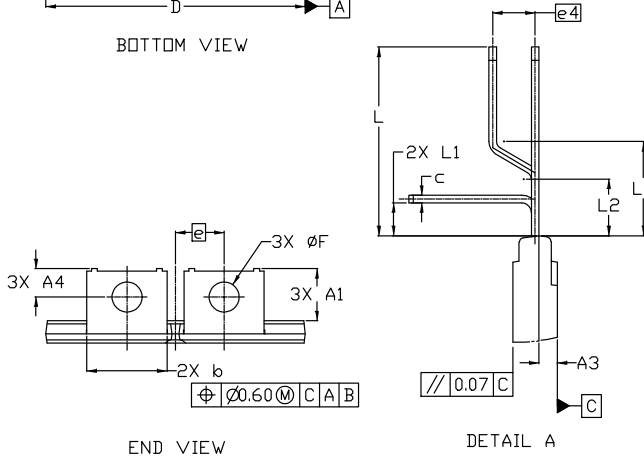
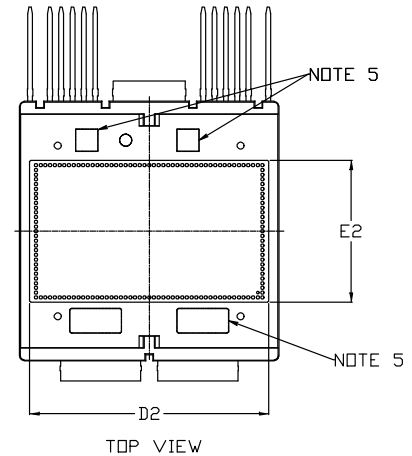
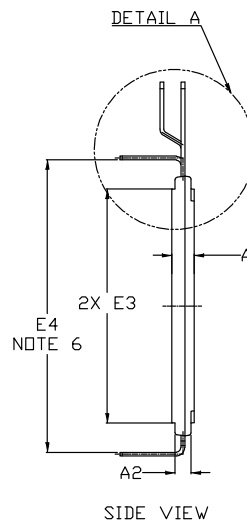
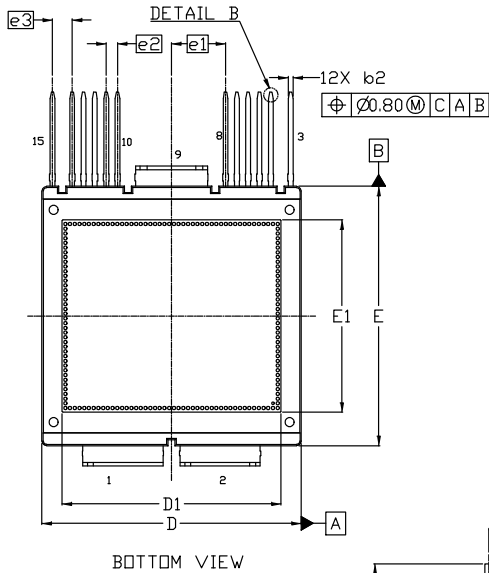
AHPM15 55x55 CASE MODHS ISSUE B

DATE 06 MAY 2022



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS D & E DO NOT INCLUDE MOLD PROTRUSIONS
4. DIMENSIONS b, b1, b2 DO NOT INCLUDE DAMBAR REMAIN.
5. MARKING AREA.
6. E4 IS FROM INNER LEAD TIP TO INNER LEAD TIP DISTANCE.



DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	4.65	4.70	4.75
A1	10.75	11.05	11.35
A2	3.20	3.40	3.60
A3	1.60	1.95	2.30
A4	5.70	6.00	6.30
b	16.90	17.00	17.10
b1	15.20	15.30	15.40
b2	0.90	1.00	1.10
b3	0.50 REF		
c	0.70	0.80	0.90
D	54.80	55.00	55.20
D1	45.80	46.80	47.80
D2	50.50	51.20	51.90

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
E	54.80	55.00	55.20
E1	40.20	41.20	42.20
E2	29.80	30.50	31.20
E3	49.40	49.60	49.80
E4	61.60	62.00	62.40
e	10.30 BSC		
e1	11.45 BSC		
e2	2.40 BSC		
e3	4.20 BSC		
e4	4.50 BSC		
F	6.45	6.50	6.55
L	19.60	20.00	20.40
L1	3.10	3.50	3.90
L2	5.70	6.00	6.30
L3	9.70	10.00	10.30
M	10° REF		

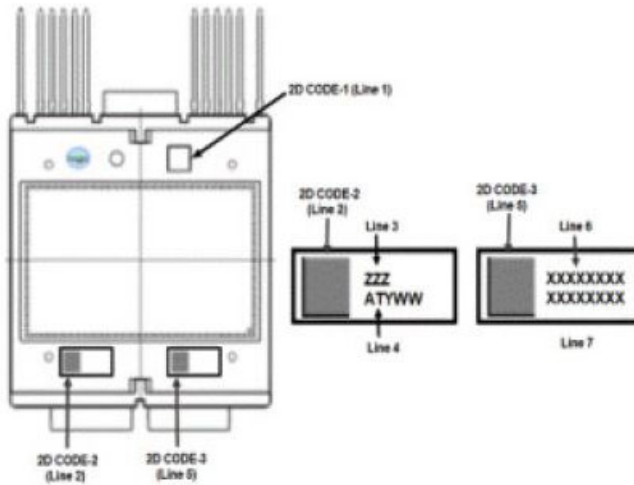
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DESCRIPTION:	AHPM15 55x55	PAGE 1 OF 2

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AHPM15 55x55
CASE MODHS
ISSUE B

DATE 06 MAY 2022

GENERIC
MARKING DIAGRAM*



ZZZ = Assembly Lot Code
 AT = Assembly & Test Location
 Y = Year
 WW = Work Week
 XXXX = Specific Device Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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